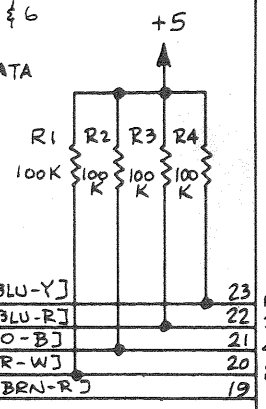
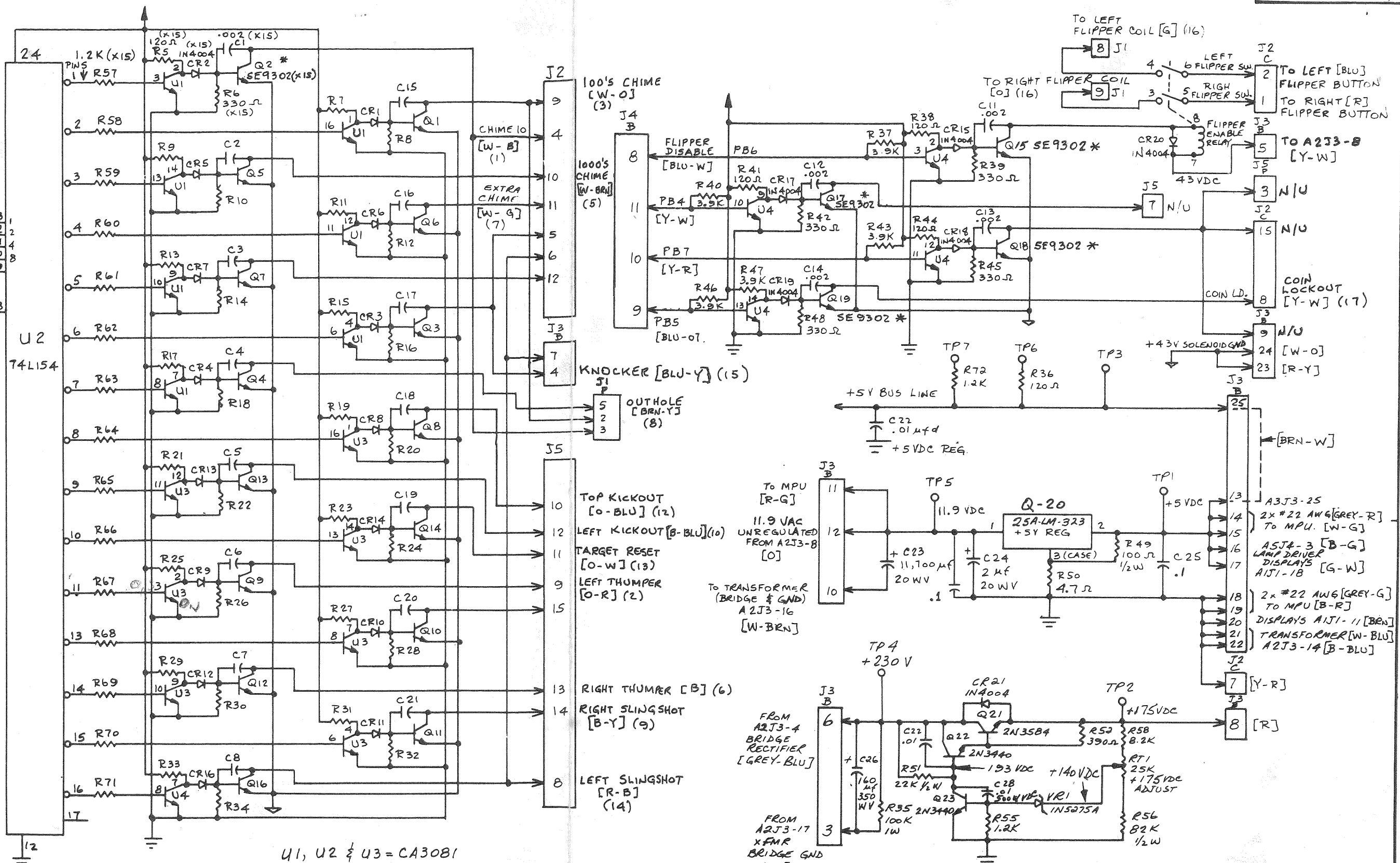


PINS 3, 4, 5 & 6
MOMENTARY
SOLENOID DATA



PIN 7
SOLENOID BANK SELECT
LOW WHEN SELECTED



SCALE:

PART MUST BE FREE OF BURRS

Unless otherwise specified, all fractional dimensions carry tolerances of plus and minus .010: All decimal dimensions plus and minus .005. Exceptions: Drilled holes plus .003 minus .001. Die casting fractional plus and minus .005: Decimal plus and minus .003. Decimal dimensions on tools plus and minus .001. Angles $\pm 30^\circ$ unless otherwise specified.

STERN ELECTRONICS INC.

1725 DIVERSEY BLVD. CHICAGO 60614

SOLENOID DRIVER
VOLTAGE REGULATOR SCHEMATIC

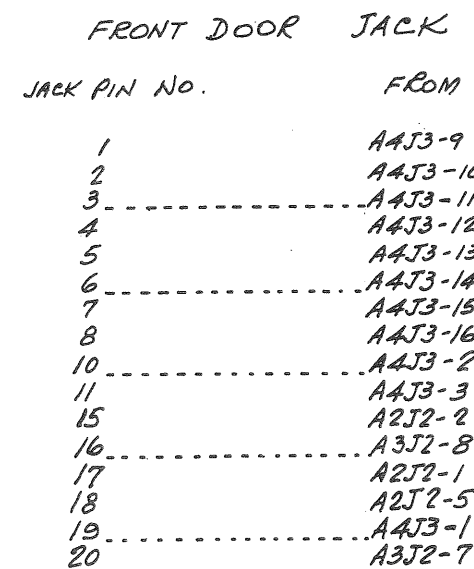
MEMORY SOLID STATE

FOR J.T. 9-20-77

DATE AM'T

MAT FIN. APPR.

ISSUE	CHANGE	DATE



N/U = NOT USED
ALL DIODES ARE 1N-4004
CAPACITOR DISC .05 MF 100V

ISSUE	CHANGE	DATE

SCALE:

PART MUST BE FREE OF BURRS

Unless otherwise specified, all fractional dimensions carry tolerances of plus and minus .010; All decimal dimensions plus and minus .005. Exceptions: Drilled holes plus .003 minus .001. Die casting fractional plus and minus .005; Decimal plus and minus .003. Decimal dimensions on tools plus and minus .001. Angles $\pm 30^\circ$ unless otherwise specified.

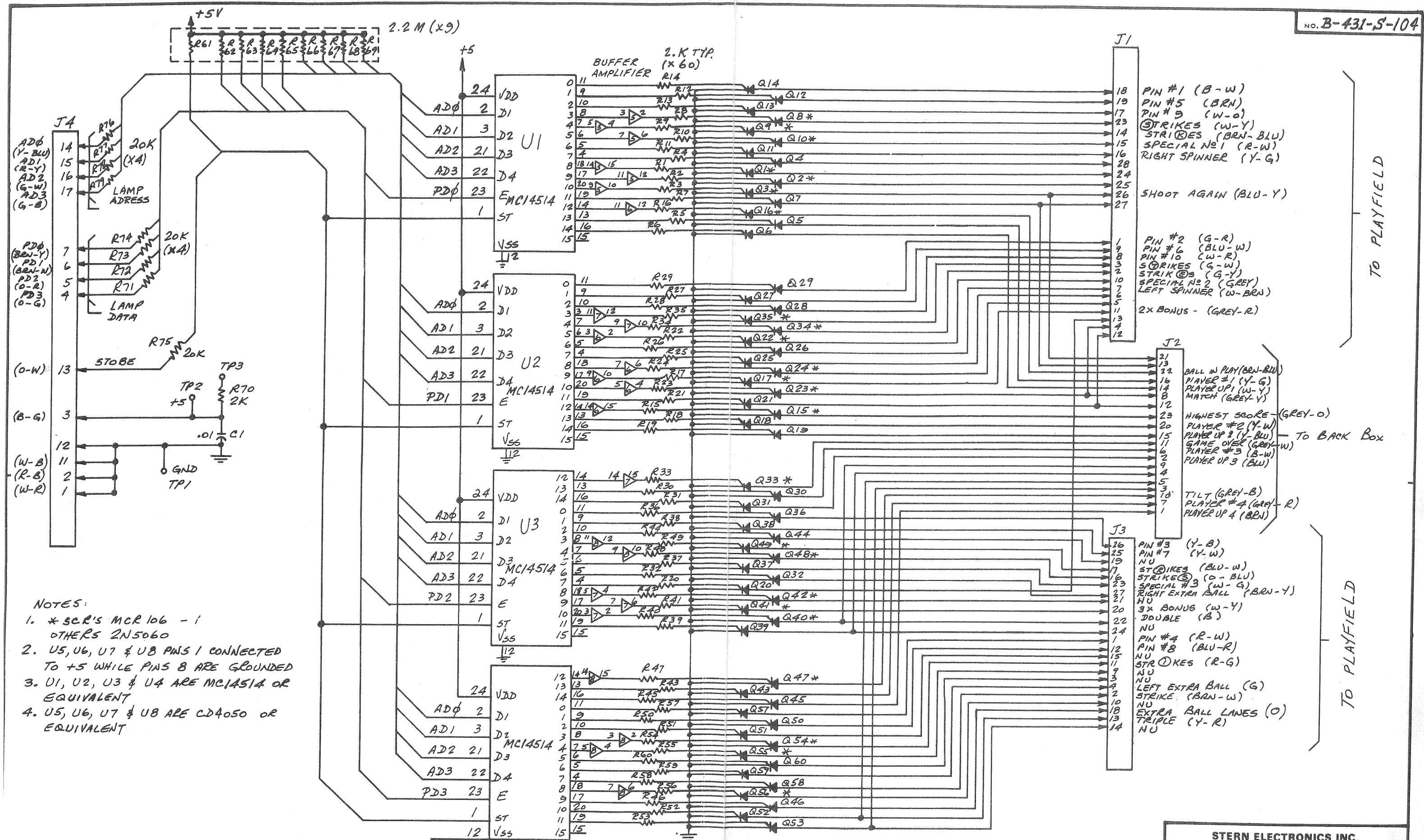
STERN ELECTRONICS INC.
1725 DIVERSEY BLVD. CHICAGO 60614

CABINET AND FRONT DOOR WIRING

FOR SOLID STATE MEMORY LANE

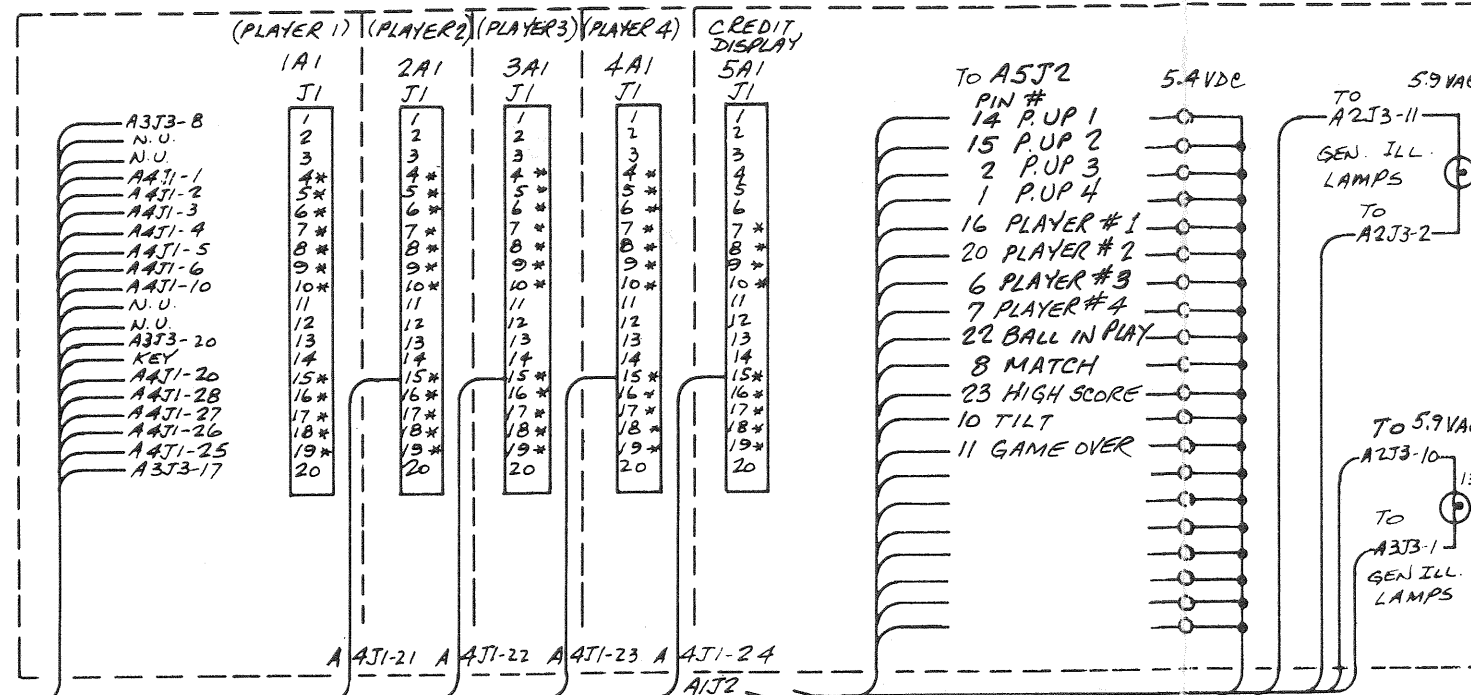
DRN. <i>J.J.</i>	DATE	AM'T <i>43</i>
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MAT <i>CH</i>	FIN. <i>CH</i>	APPR.
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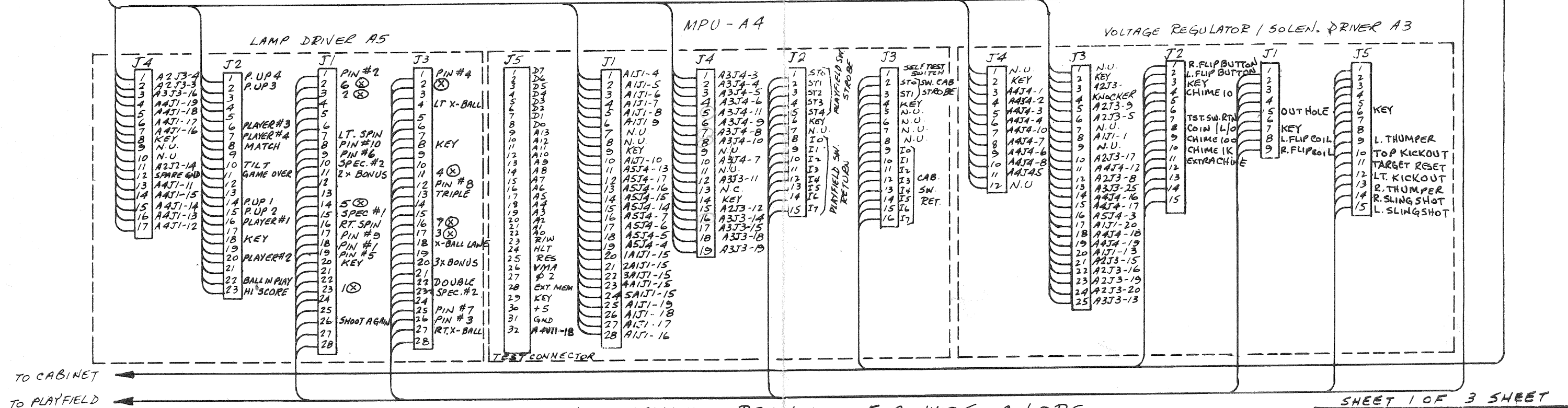
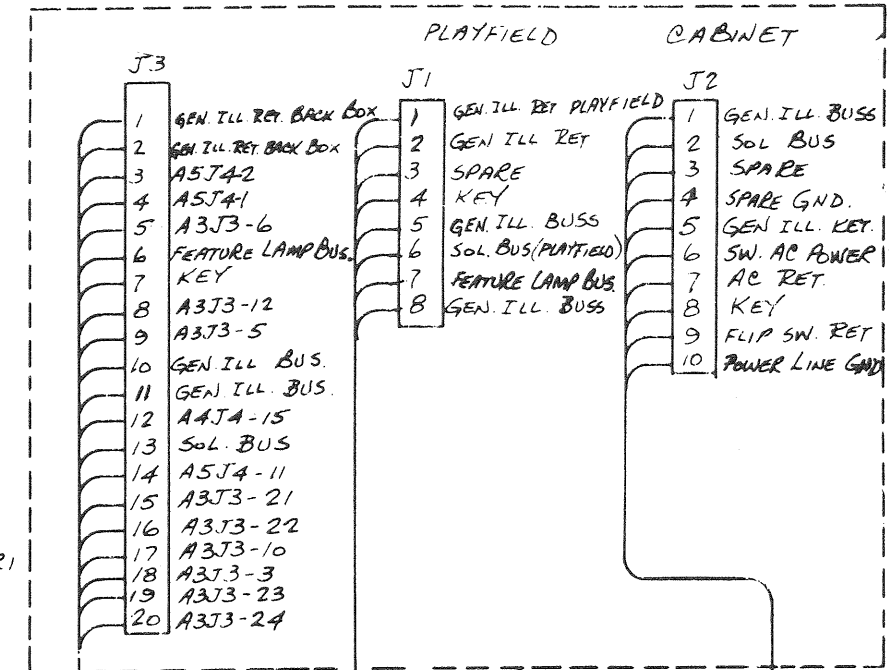


AI INSERT

FROM	A152 PIN
A2J3-6	1
A5J2-14	2
A2J3-2	3
A2J3-11	4
A5J2-23	5
A5J2-15	6
A5J2-22	7
A5J2-8	8
A5J2-1	9
A5J2-2	10
A5J2-11	11
	12
A5J2-10	13
A5J2-7	14
A5J2-6	15
A5J2-20	16
A5J2-16	17
A2J3-10	18
A2J3-1	19
	20
	21
	22
	23



A2 TRANSFORMER



SEE INDIVIDUAL SCHEMATIC DRAWINGS FOR WIRE COLORS.

SHEET 1 OF 3 SHEET

STERN ELECTRONICS INC.

1725 DIVERSEY BLVD. CHICAGO 60614

WIRING DIAGRAM

MEMORY LANE SOLID STATE

FOI		
11		

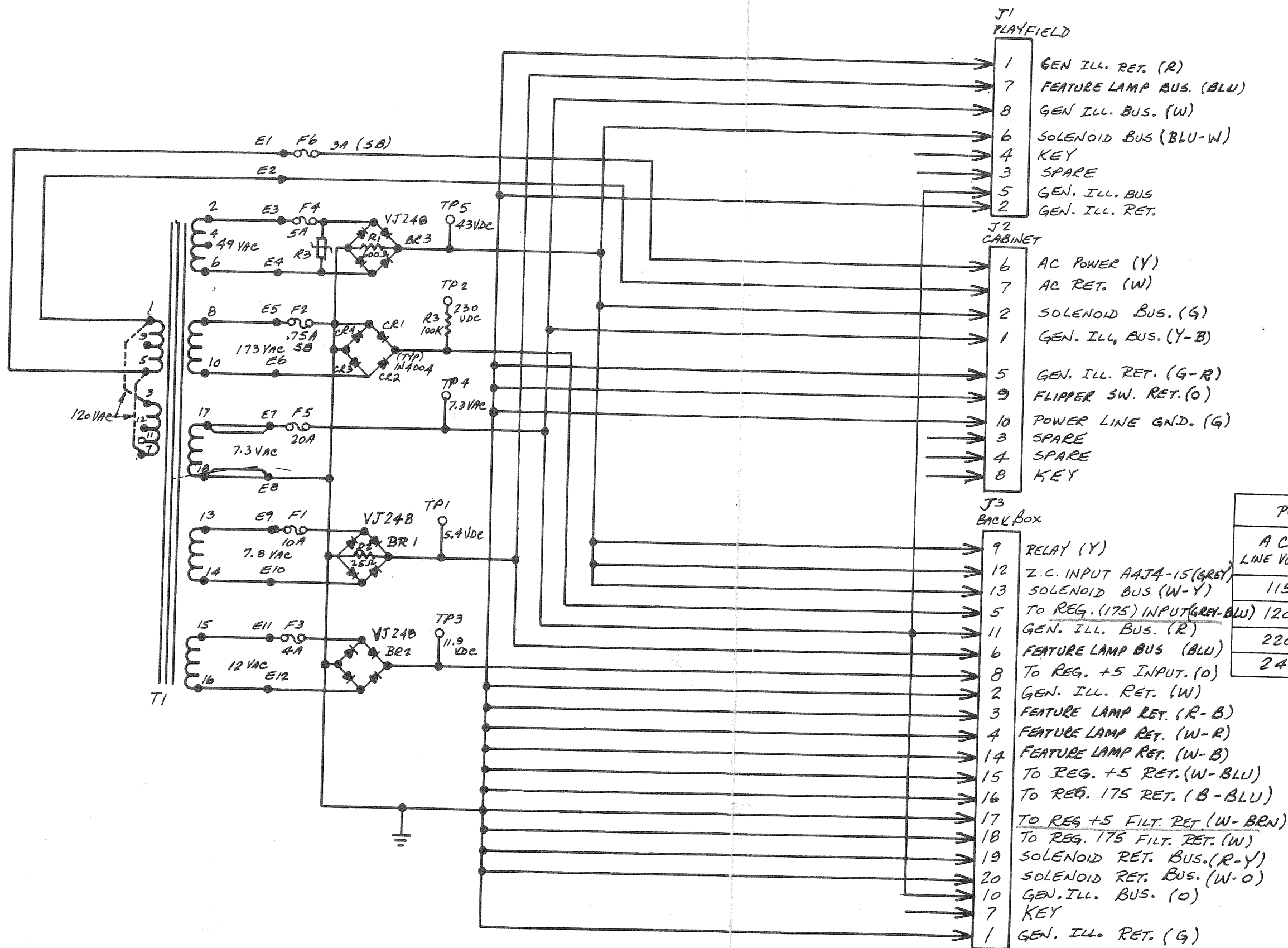
DRN.	DATE	AM'T
77		

MAT <i>LAD</i>	FIN. <i>LAD</i>	APPR.
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SCALE: *447*

PART MUST BE FREE OF BURRS

Unless otherwise specified, all fractional dimensions carry tolerances of plus and minus .010: All decimal dimensions plus and minus .005. Exceptions: Drilled holes plus .003 minus .001. Die casting fractional plus and minus .005: Decimal plus and minus .003. Decimal dimensions on tools plus and minus .001. Angles $\pm 30'$ unless otherwise specified.



ABBREVIATIONS USED
GEN ILL. = GENERAL ILLUMINAT
RET. = RETURN
GND = GROUND
REG = REGULATED
Z.C. = ZERO CROSSING

POWER LINE CONNECTIONS		
A C LINE VOLTAGE	STRAP TERMINALS	APPLY POWER TO TERMINALS
115	1 TO 3 AND 9 TO 11	1 AND 9
120	1 TO 3 AND 5 TO 7	1 AND 5
220	3 TO 5	1 AND 12
240	3 TO 5	1 AND 7

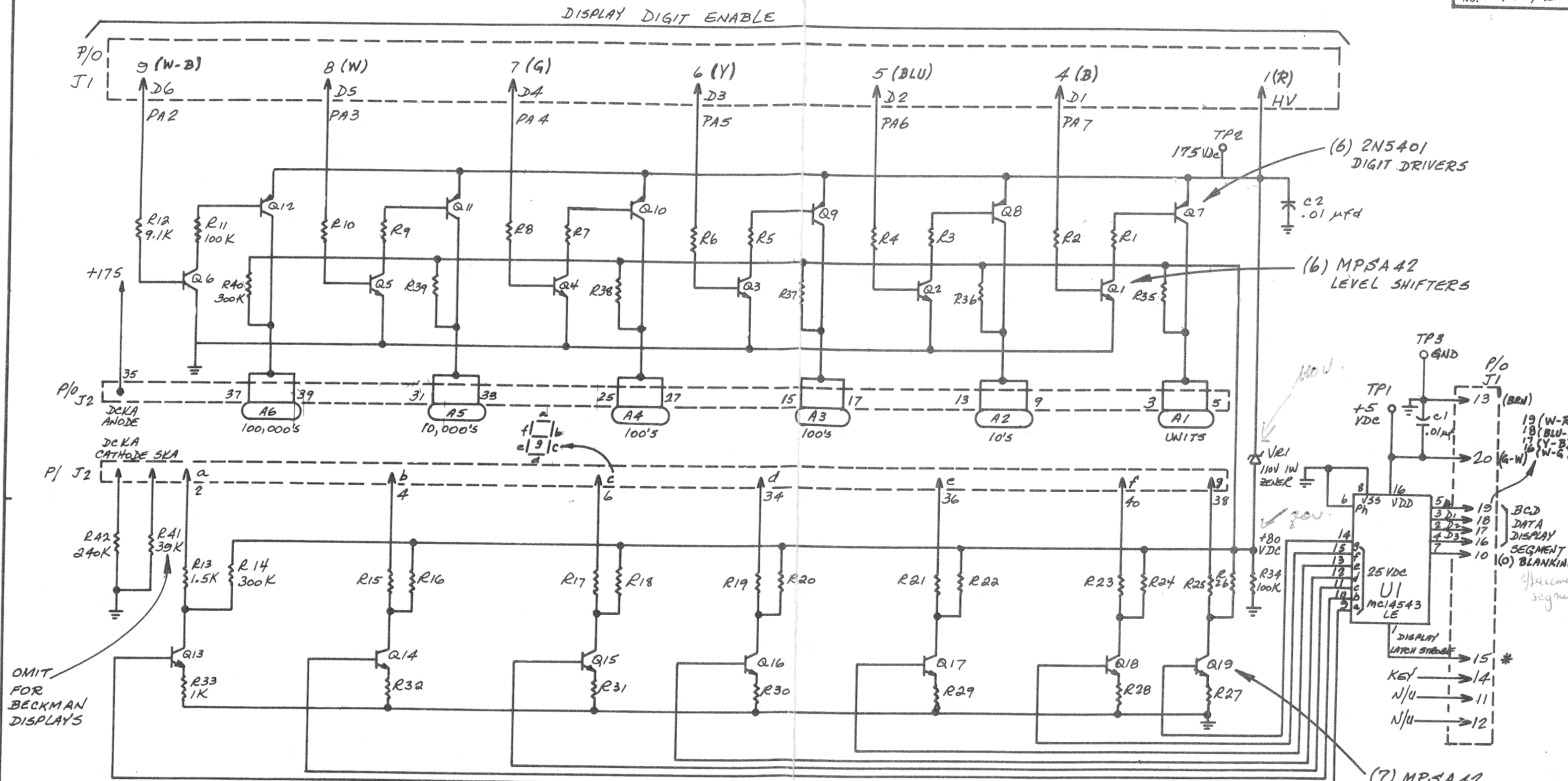
ISSUE A	UP-DATED	CHANGE	DATE 1-78
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SCALE: *AS*

PART MUST BE FREE OF BURRS

Unless otherwise specified, all fractional dimensions carry tolerances of plus and minus .010: All decimal dimensions plus and minus .005. Exceptions: Drilled holes plus .003 minus .001. Die casting fractional plus and minus .005: Decimal plus and minus .003. Decimal dimensions on tools plus and minus .001. Angles $\pm 30^\circ$ unless otherwise specified.

STERN ELECTRONICS INC.		
1725 DIVERSEY BLVD. CHICAGO 60614		
POWER TRANSFORMER MODULE SCHEMATIC		
FOR MEMORY LANE SOLID STATE		
DRN. J. J.	DATE 9-20-77	AM'T <i>W</i>
MAT <i>W</i>	FIN. <i>W</i>	APPR.



*
PIN 15 WIRE COLOR
1ST. PLAYER B-W
2ND. PLAYER O-BLU
3RD. PLAYER R-Y
4TH. PLAYER BRN-O
MATCH/BALL R-B

				SCALE: <i>L#7</i>	PART MUST BE FREE OF BURRS
				Unless otherwise specified, all fractional dimensions carry tolerances of plus and minus .010: All decimal dimensions plus and minus .005. Exceptions: Drilled holes plus .003 minus .001. Die casting fractional plus and minus .005: Decimal plus and minus .003. Decimal dimensions on tools plus and minus .001. Angles $\pm 30^\circ$ unless otherwise specified.	
ISSUE <i>A</i>	<i>UPDATED</i>	CHANGE		DATE <i>1-78</i>	

STERN ELECTRONICS INC.
1725 DIVERSEY BLVD. CHICAGO 60614

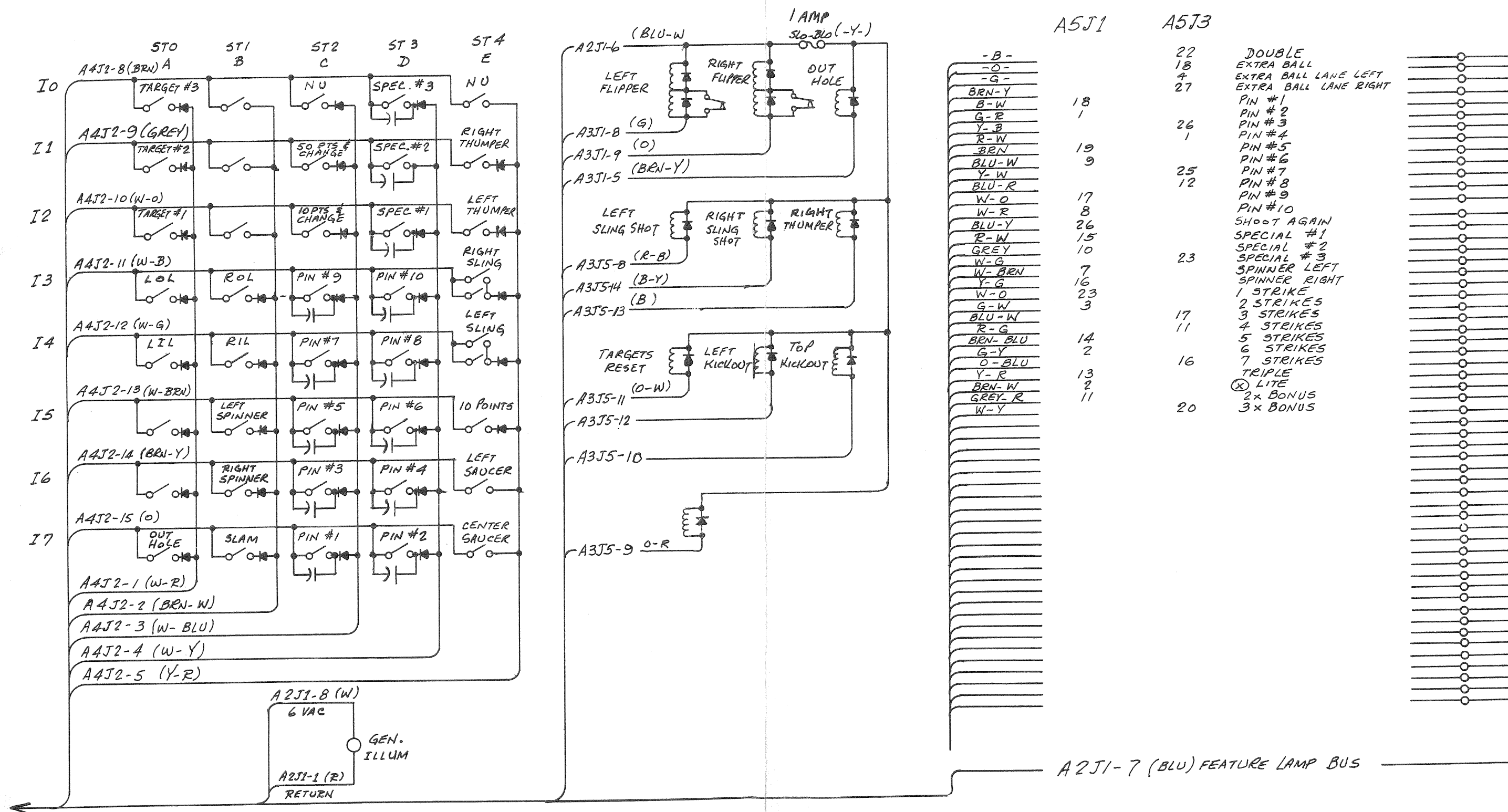
DISPLAY BOARD SCHEMATIC

MEMORY LANE SOLID STATE

FOR

DRN. J.J. 9-20-77 AM'T L#

MAT L# FIN. L# APPR.



PLAYFIELD