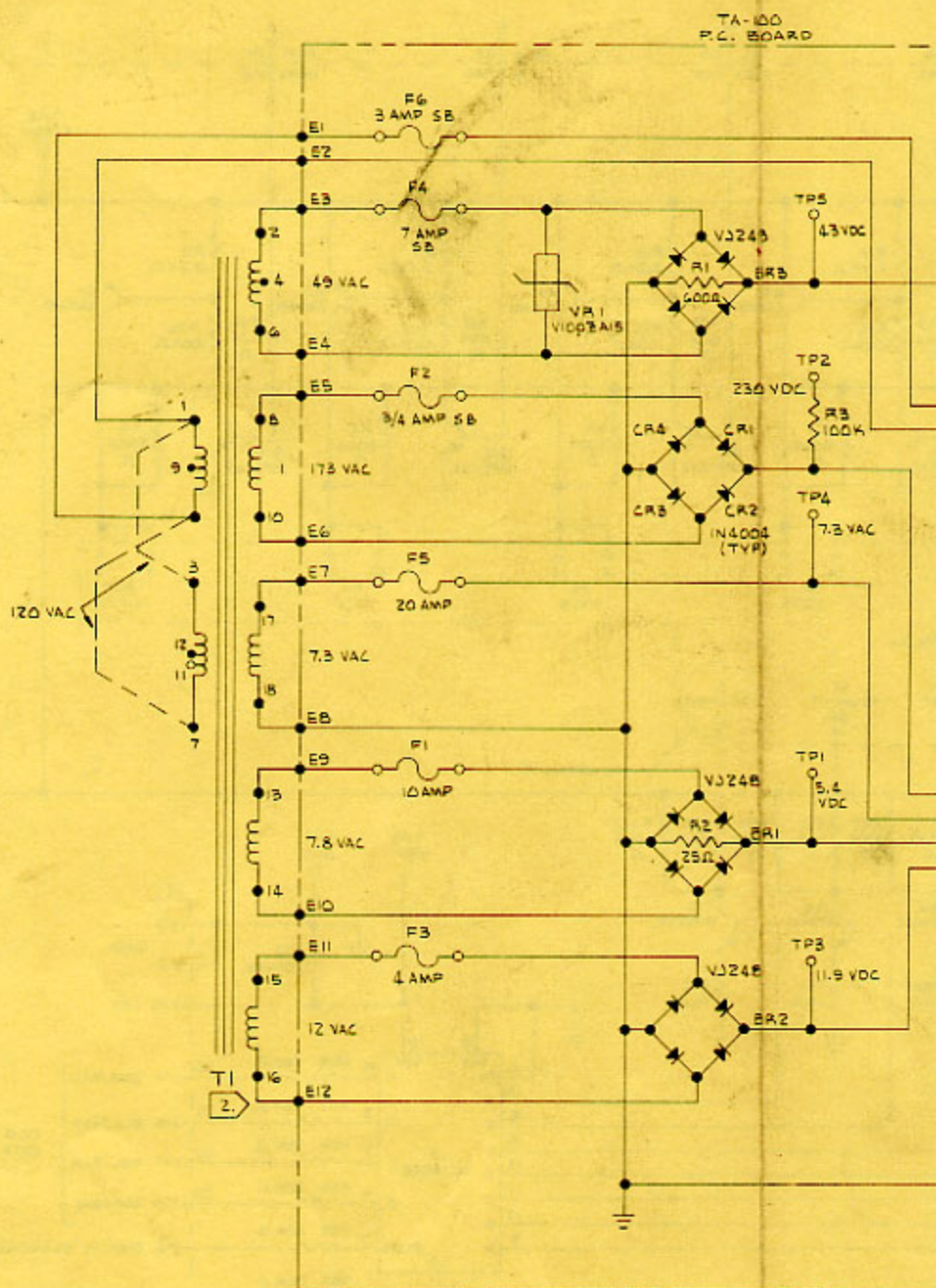


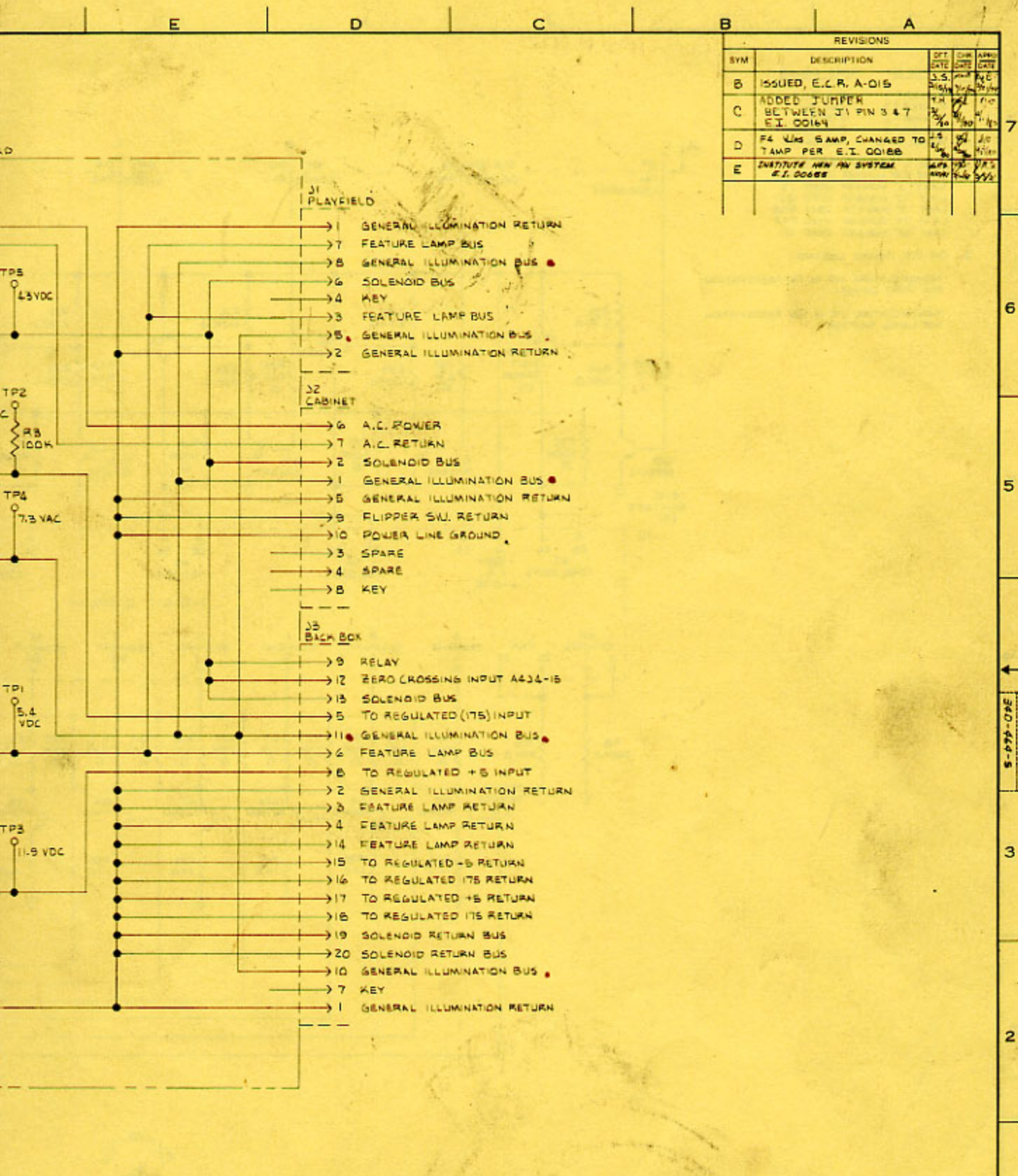
NOTES:

1. POWER LINE CONNECTIONS:

AC LINE VOLTAGE	STRAP TERMINALS	APPLY POWER TO TERMINALS
115	1 TO 3 AND 9 TO 11	1 AND 9
120	1 TO 3 AND 5 TO 7	1 AND 5
220	3 TO 5	1 AND 12
240	3 TO 5	1 AND 7

2. T1 IS NOT LOCATED ON PRINTED CIRCUIT BOARD.





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NOTES:

1. PIN IS WIRE COLOR

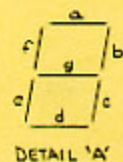
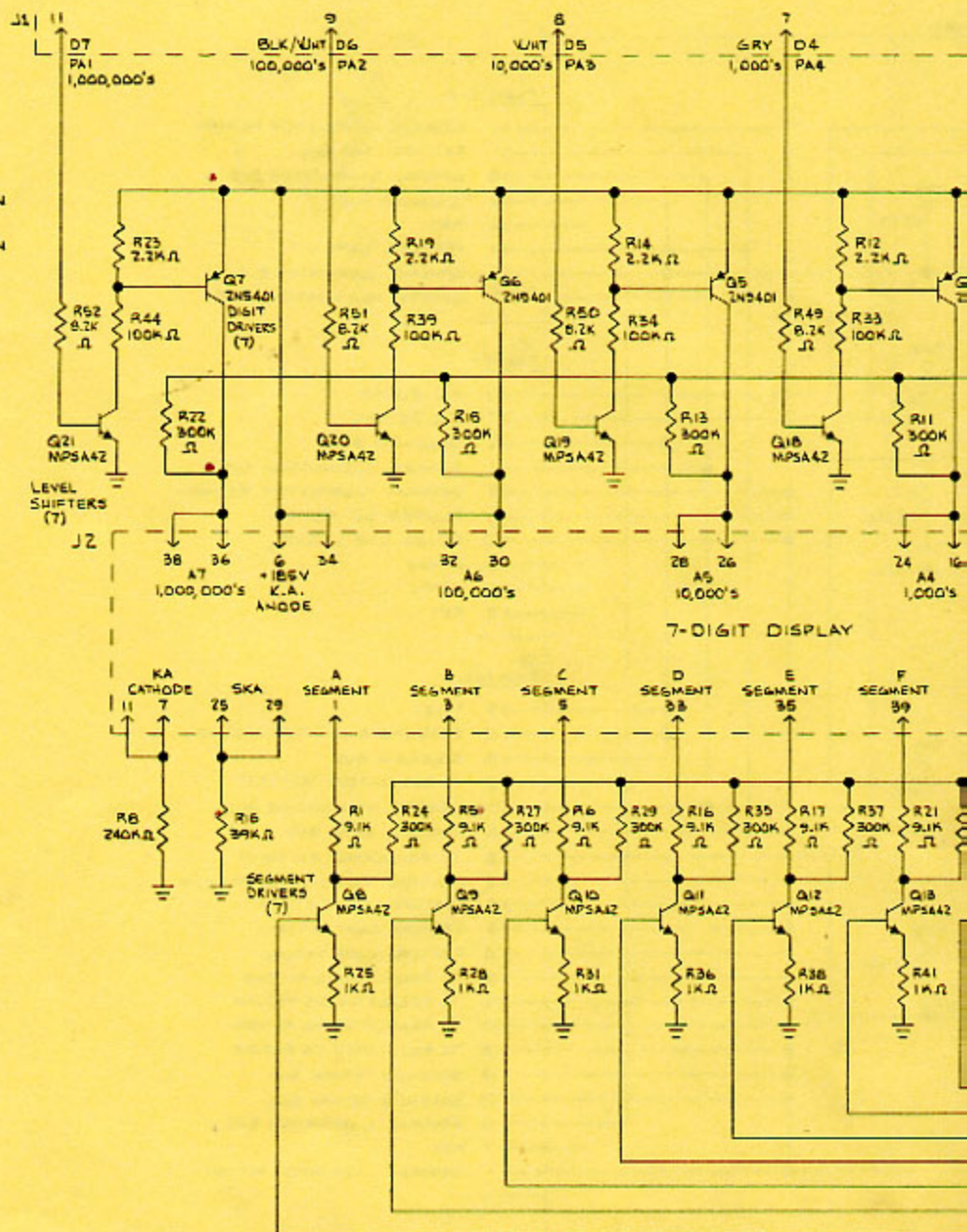
PLAYER # 1 BLK/WHT
 PLAYER # 2 ORG/BLU
 PLAYER # 3 RED/YEL
 PLAYER # 4 BRN/ORG
 MATCH/BALL RED/BLACK

2. LAST 'C' NUMBER USED C2
 LAST 'CR' NUMBER USED CR1
 LAST 'J' NUMBER USED J2
 LAST 'Q' NUMBER USED Q21
 LAST 'R' NUMBER USED R55
 LAST 'TP' NUMBER USED TP3
 LAST 'U' NUMBER USED U1

3. ON P.C. BOARD LEGEND:

NOMENCLATURE DD IS THE ABBREVIATION FOR DIGIT DRIVER.

NOMENCLATURE LS IS THE ABBREVIATION FOR LEVEL SHIFTER.



NOTES:

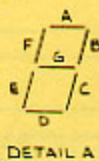
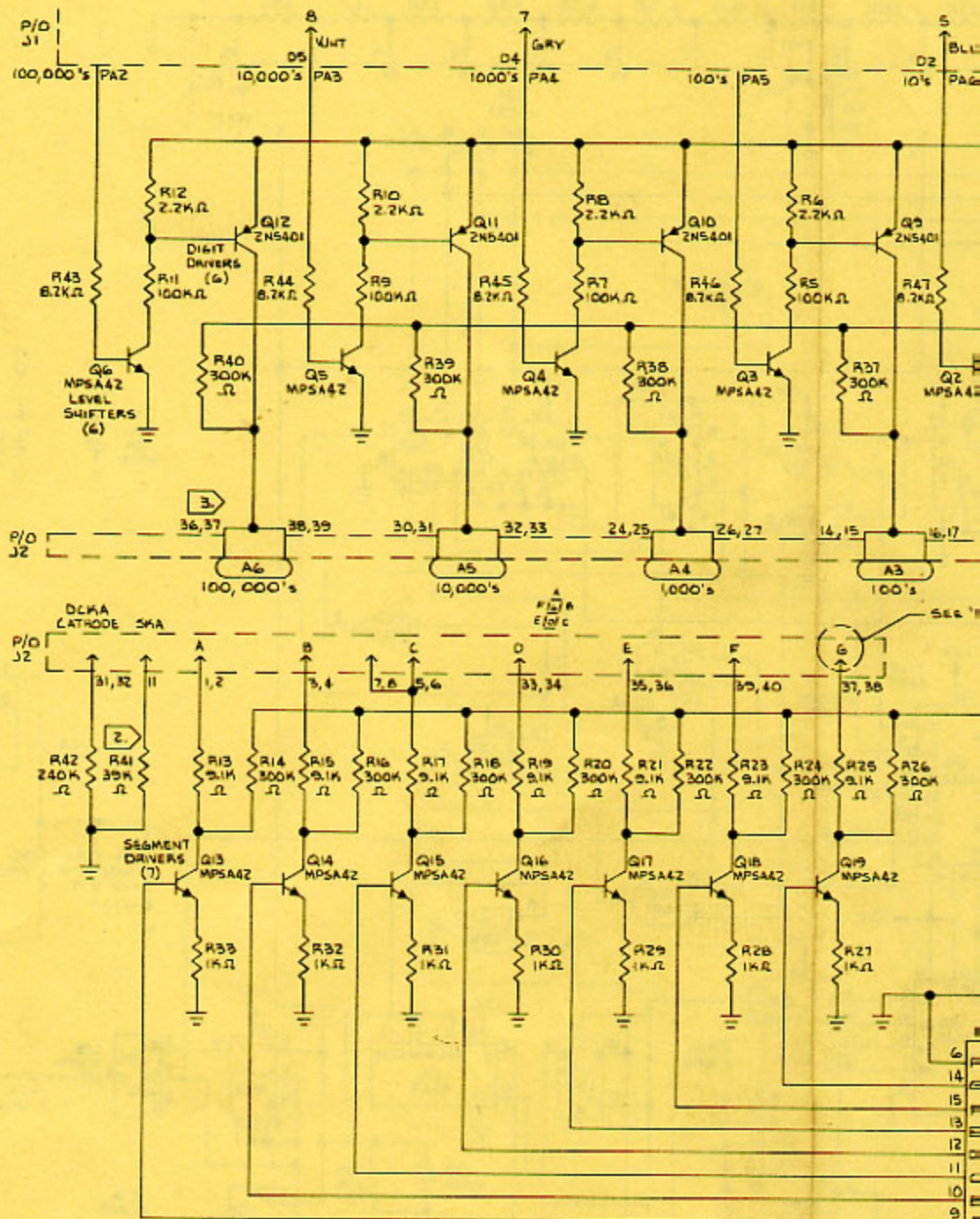
1.	PIN IS	WIRE COLOR
PLAYER #1	BLK/WHT	
PLAYER #2	ORG/BLU	
PLAYER #3	RED/YEL	
PLAYER #4	BRN/ORG	
MATCH/BALL	RED/BLK	

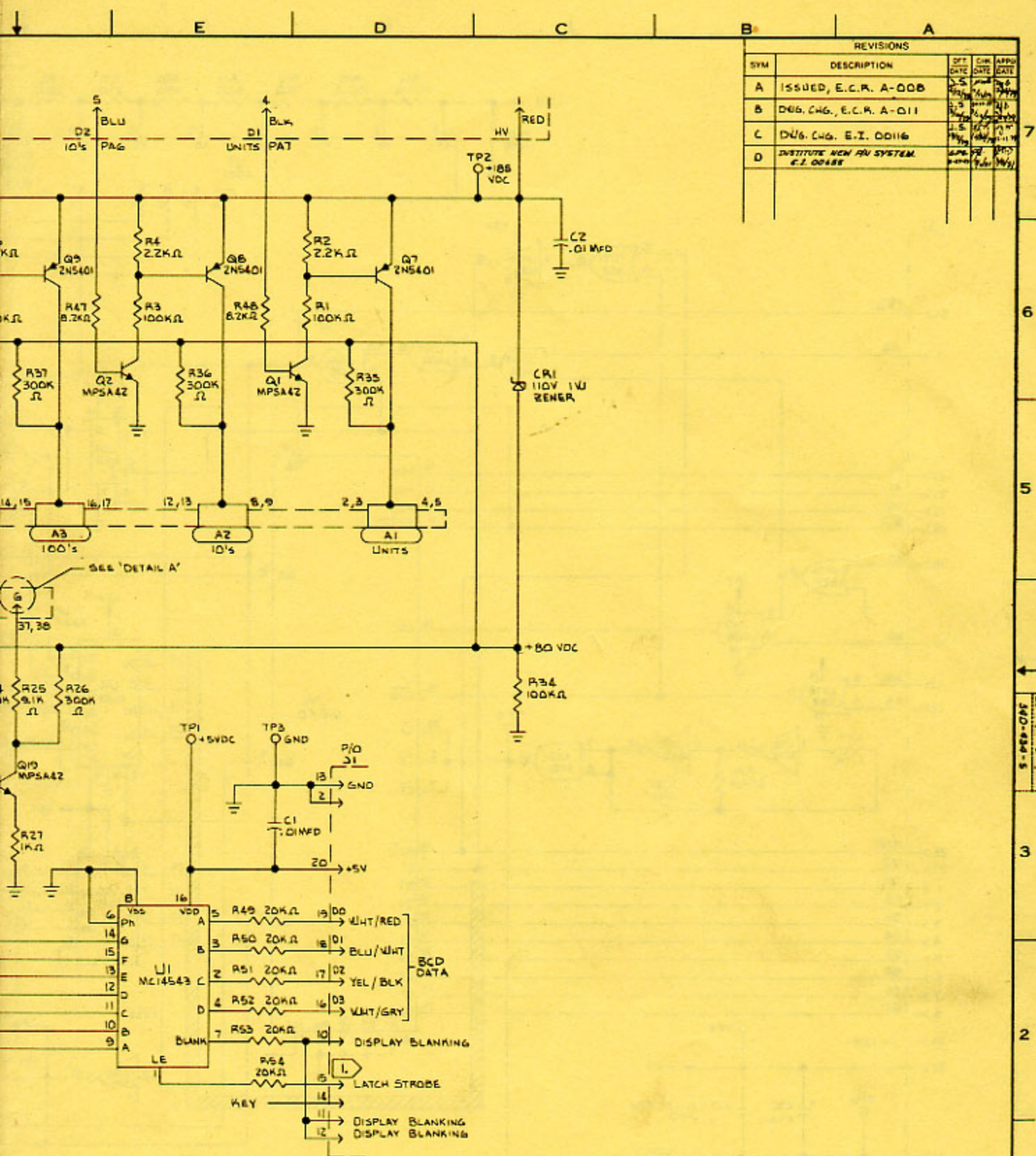
2. OMIT FOR BECKMAN DISPLAYS.

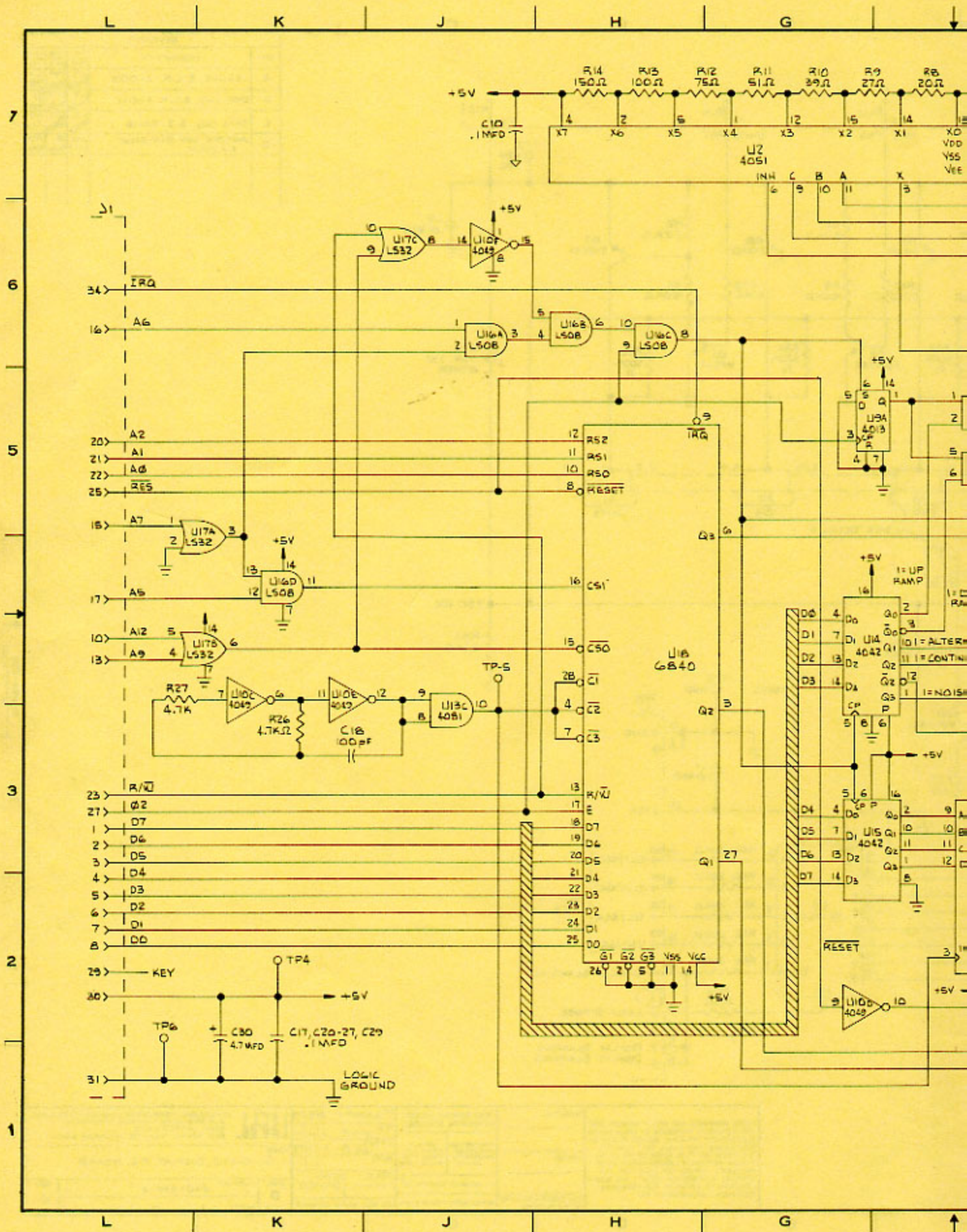
3. 36, 37 DENOTES

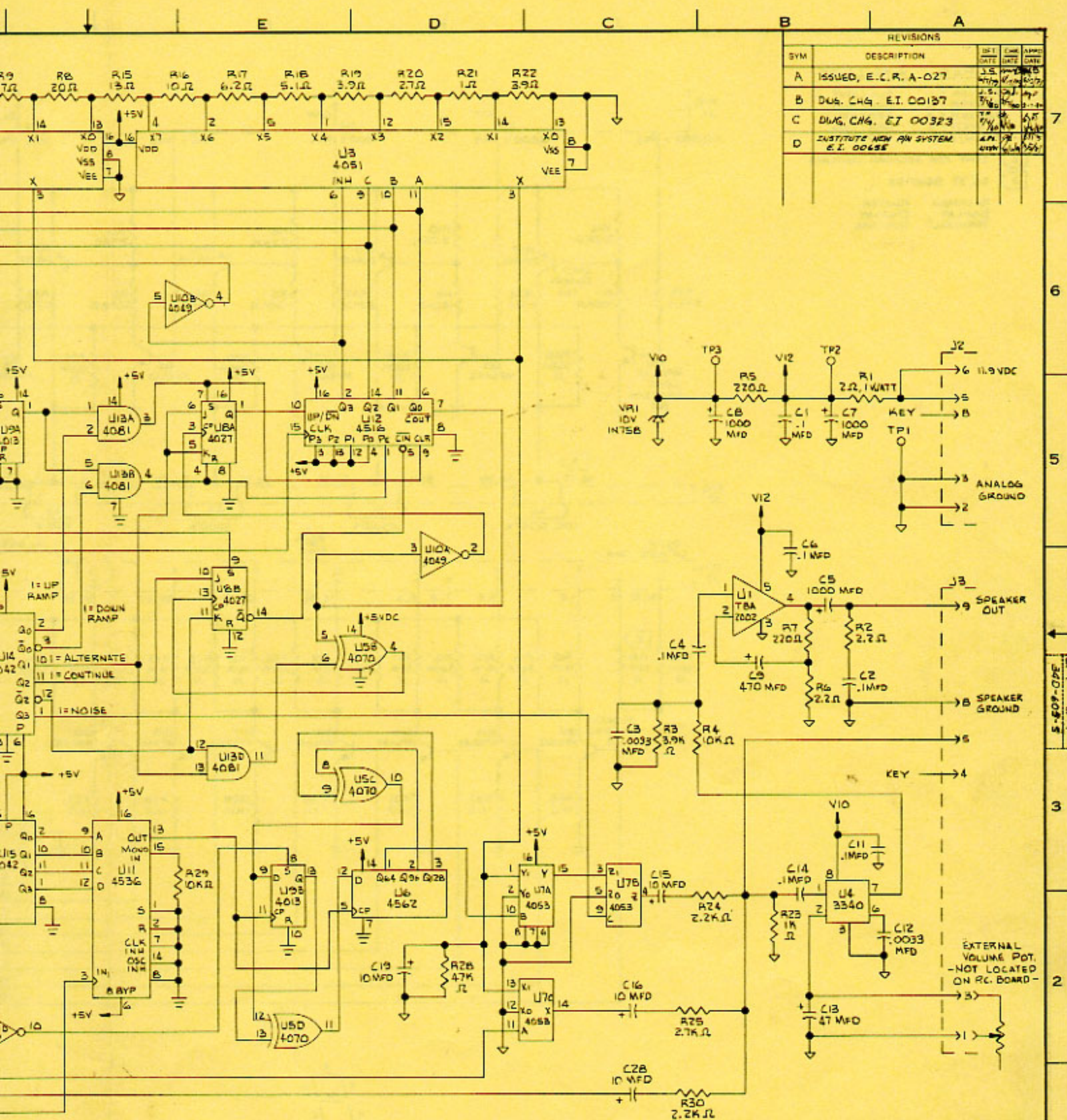
BECKMAN
DISPLAY
TERMINAL*

PANTEK
DISPLAY
TERMINAL





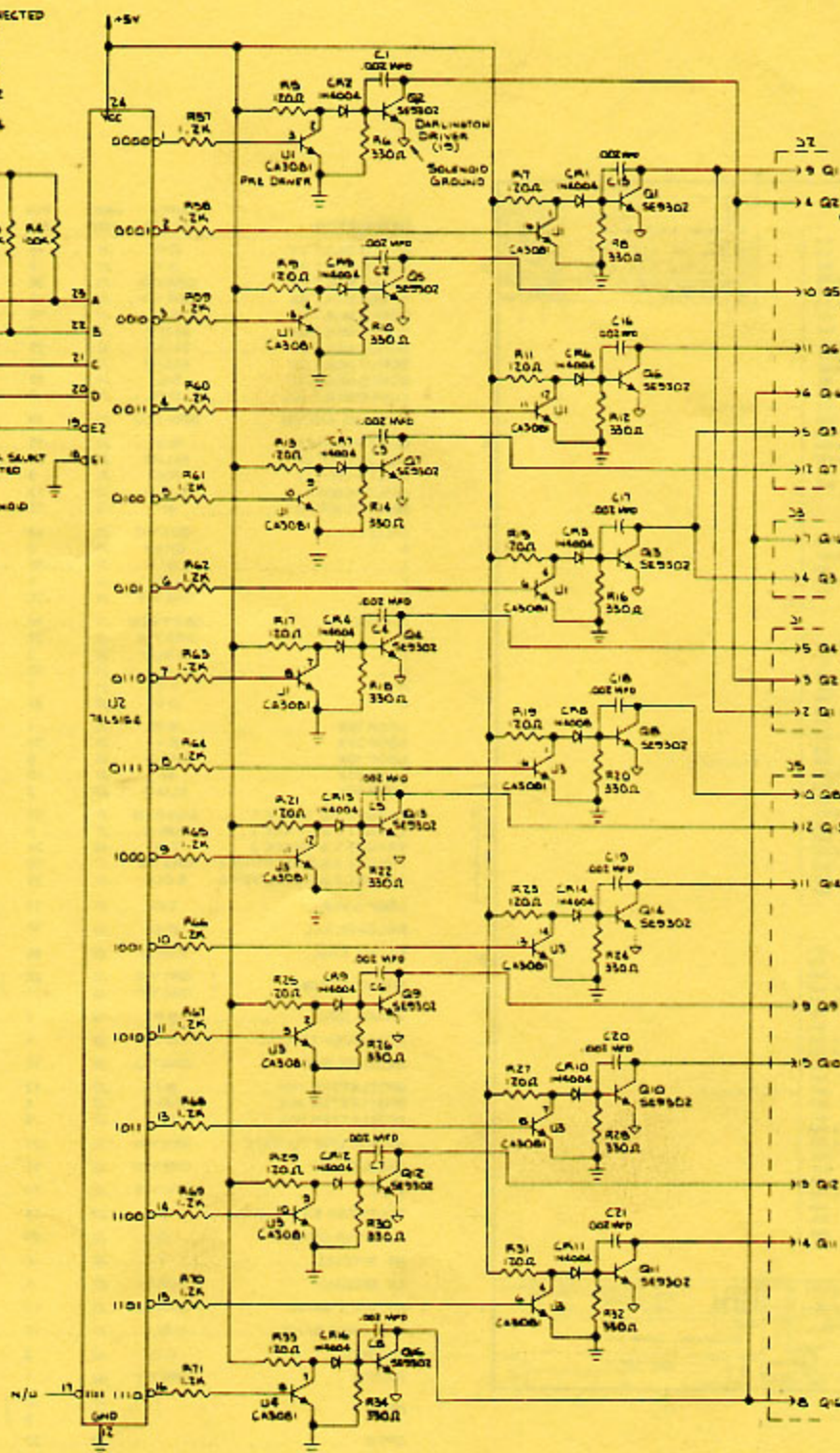
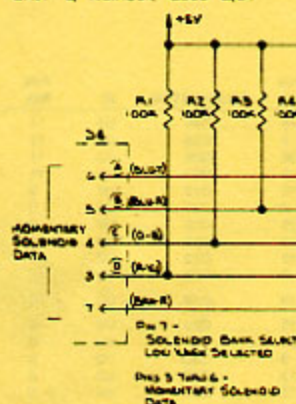




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NAME SCHEMATIC, ELECTRONIC SOUNDS		DWG NO. 340-605-5		REV. D		SHEET 1 OF 1		DATE 12-1-65	

MODEL

1. 2N4104 is an APPROVED ALTERNATE PART FOR TRANSISTORS Q1-Q12.
2. PIN 18 OF U1, U3, U4 IS EMITTER CONNECTED TO GROUND.
3. THE TAB ON 569302 IS CONNECTED TO THE COLLECTOR OF THE TRANSISTOR.
4. LAST 'R' NUMBER USED R75
LAST 'C' NUMBER USED C29
LAST 'CR' NUMBER USED CR22
LAST 'U' NUMBER USED U4
LAST 'Q' NUMBER USED Q24



RT. THUMPER (G-1)
L. THUMPER (G-2)

L. SLINGSHOT (G-3)

RT. SLINGSHOT (G-4)

KNOCKER

DUTHOLE REFLEX (B-BL)

TURNSTYLE (B-D)

TOP DROP BANK (B-E)

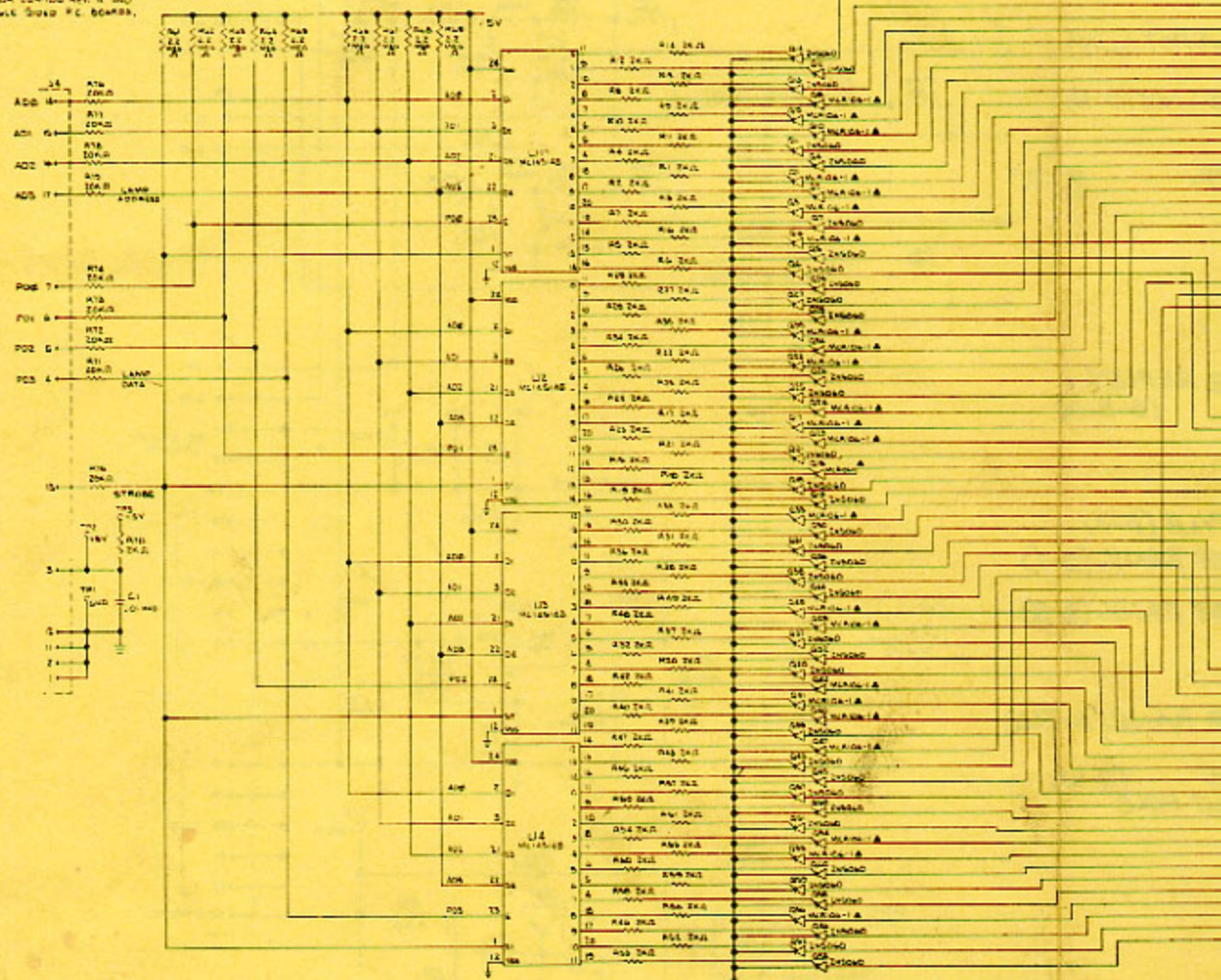
MID DROP BANK (B-F)

RT. DROP BANK (B-G)

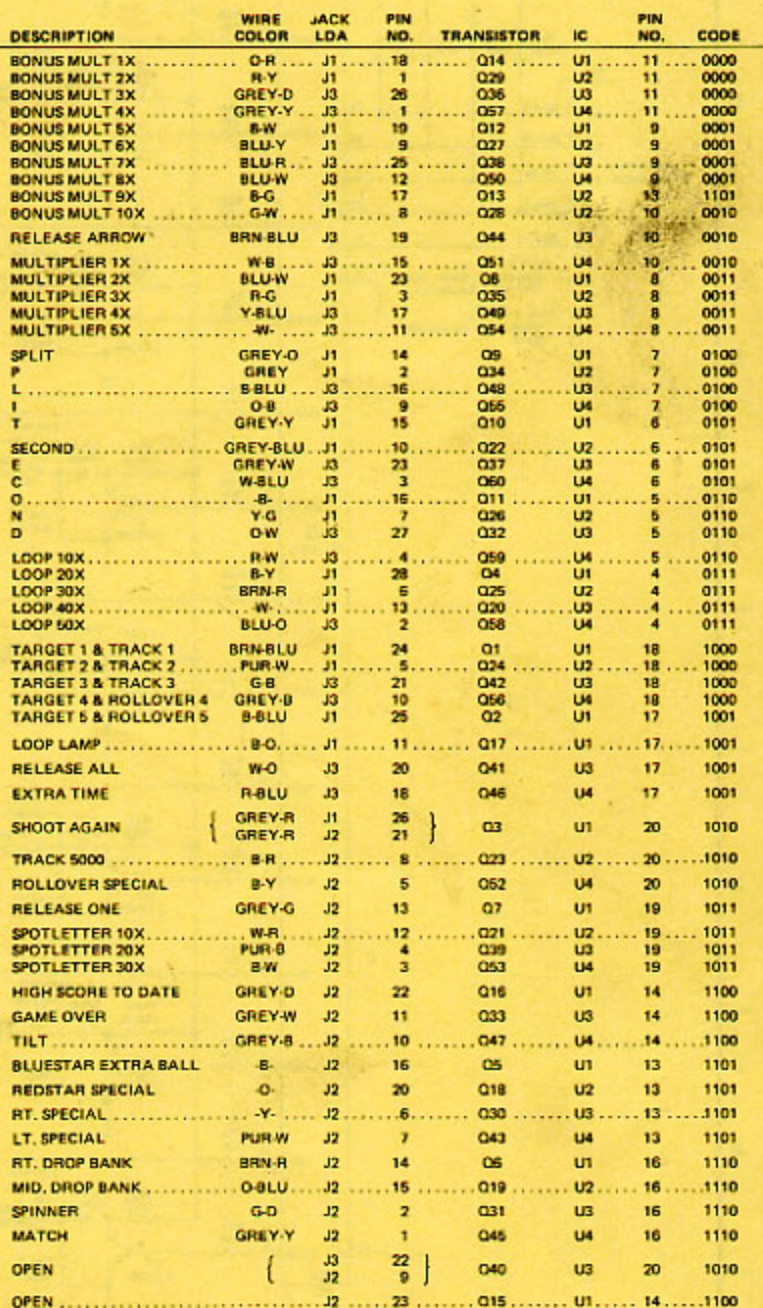
CAPTIVE KICKOUT HOLE (B-H)

NOTES:

1. ▲ INDICATES MAXIMUM TRANSISTOR.
2. This Document For LDA-100 Rev. A and LDA-Rev. A Single Sided P.C. BOARD.



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L

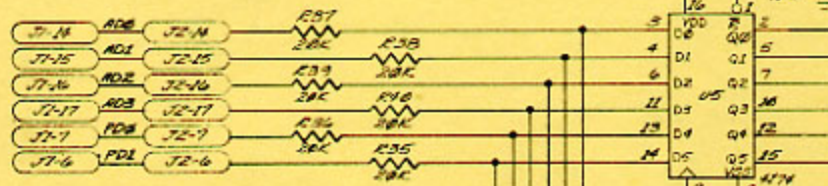
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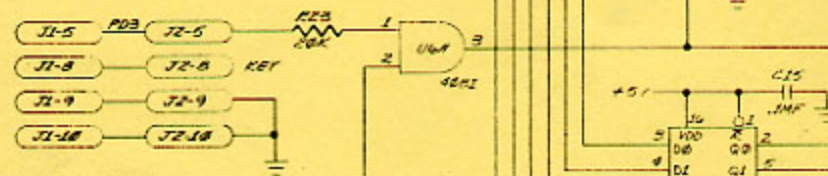
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G

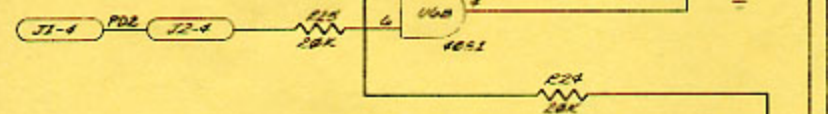
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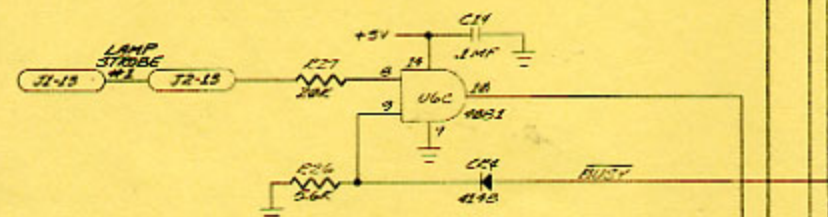
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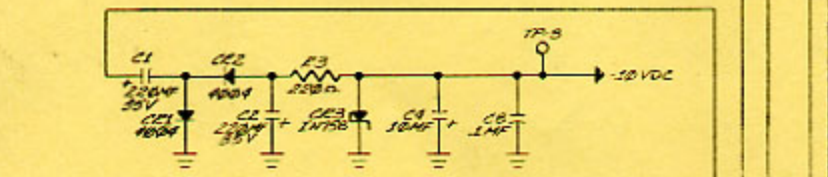
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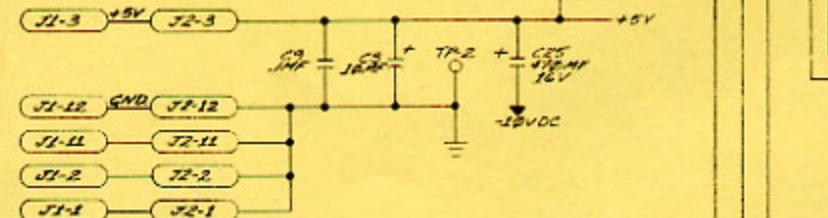
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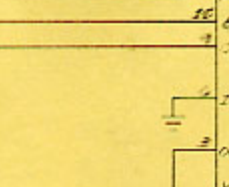
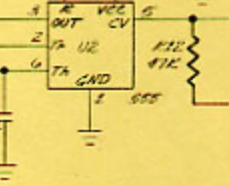
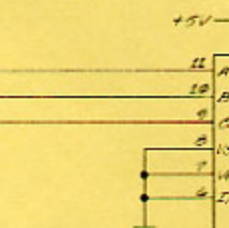
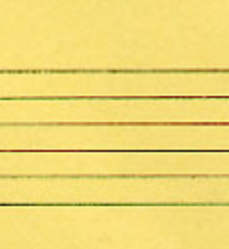
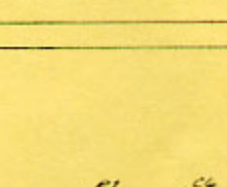
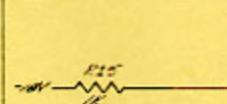
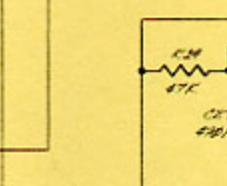
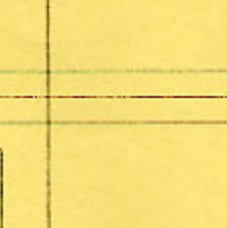
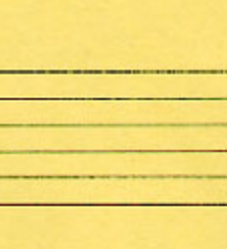
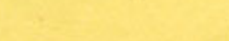
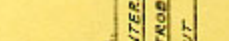
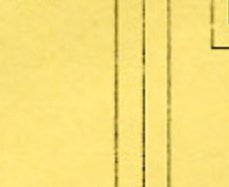
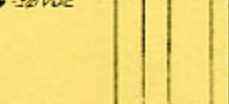
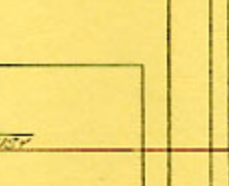
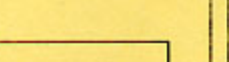
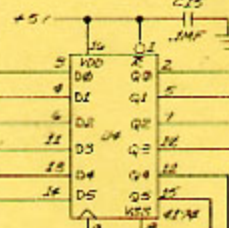
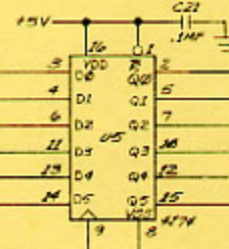
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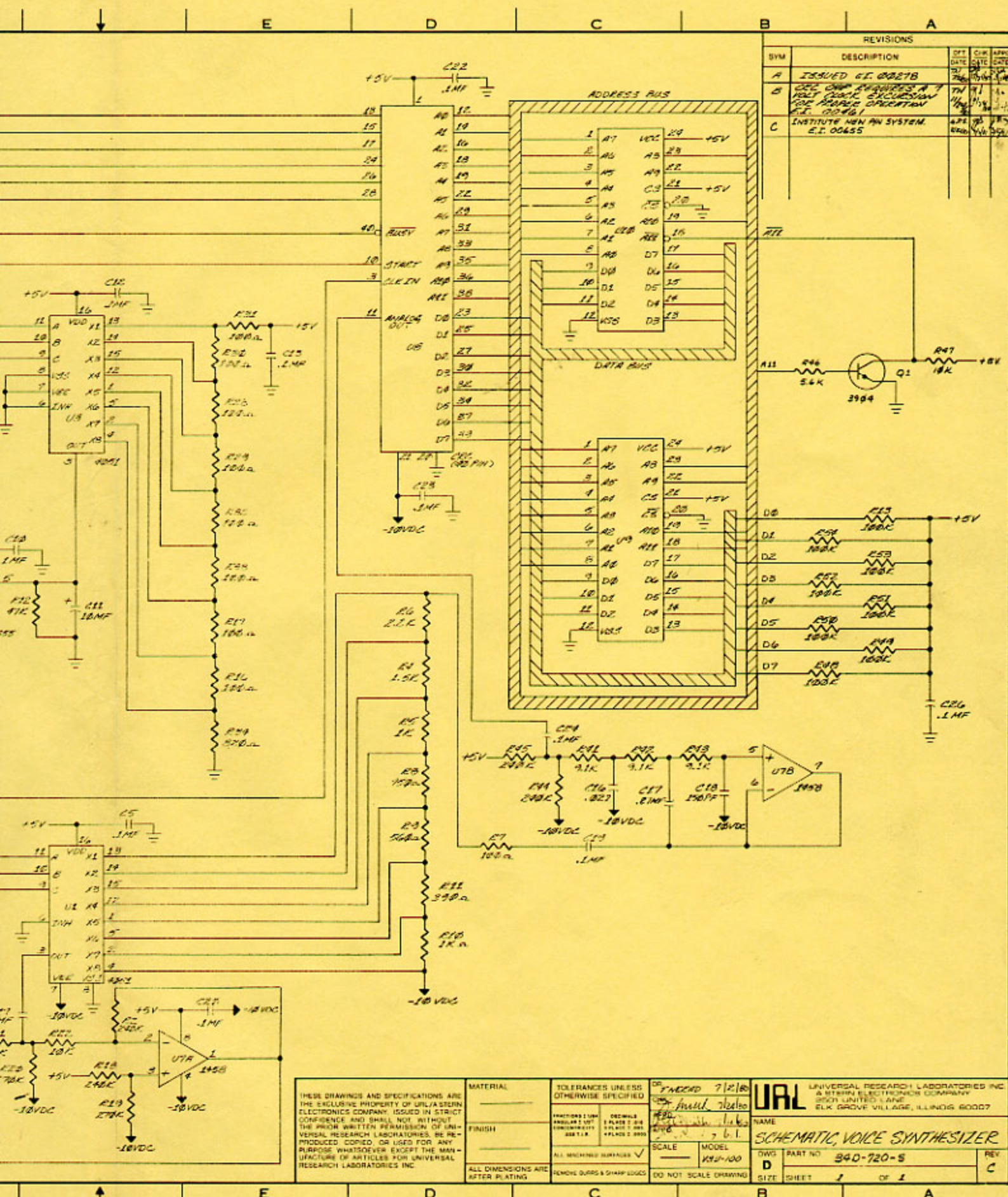


2



1





REVISIONS				
SYM	DESCRIPTION	DATE	BY	APP'D
A	ISSUED G.E. 00278	11/11/60	W.H.	W.H.
B	DEL. COMP. EXCERPTS A 7 VOLT. EXCERPTS A 7 FOR PAPER OPERATION G.E. 00461	11/11/60	W.H.	W.H.
C	INSTITUTE NEW HW SYSTEM G.E. 00655	11/11/60	W.H.	W.H.

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7

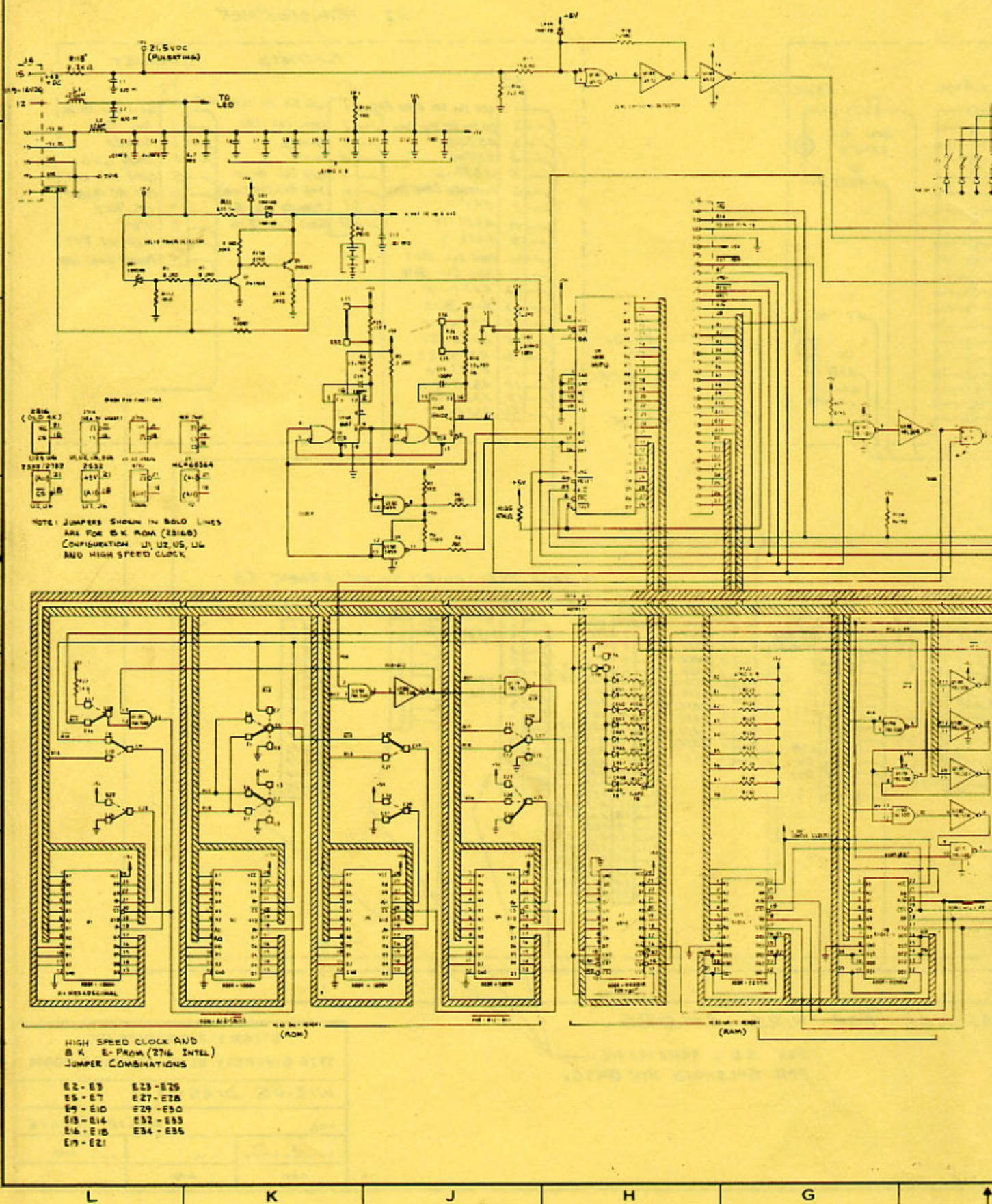
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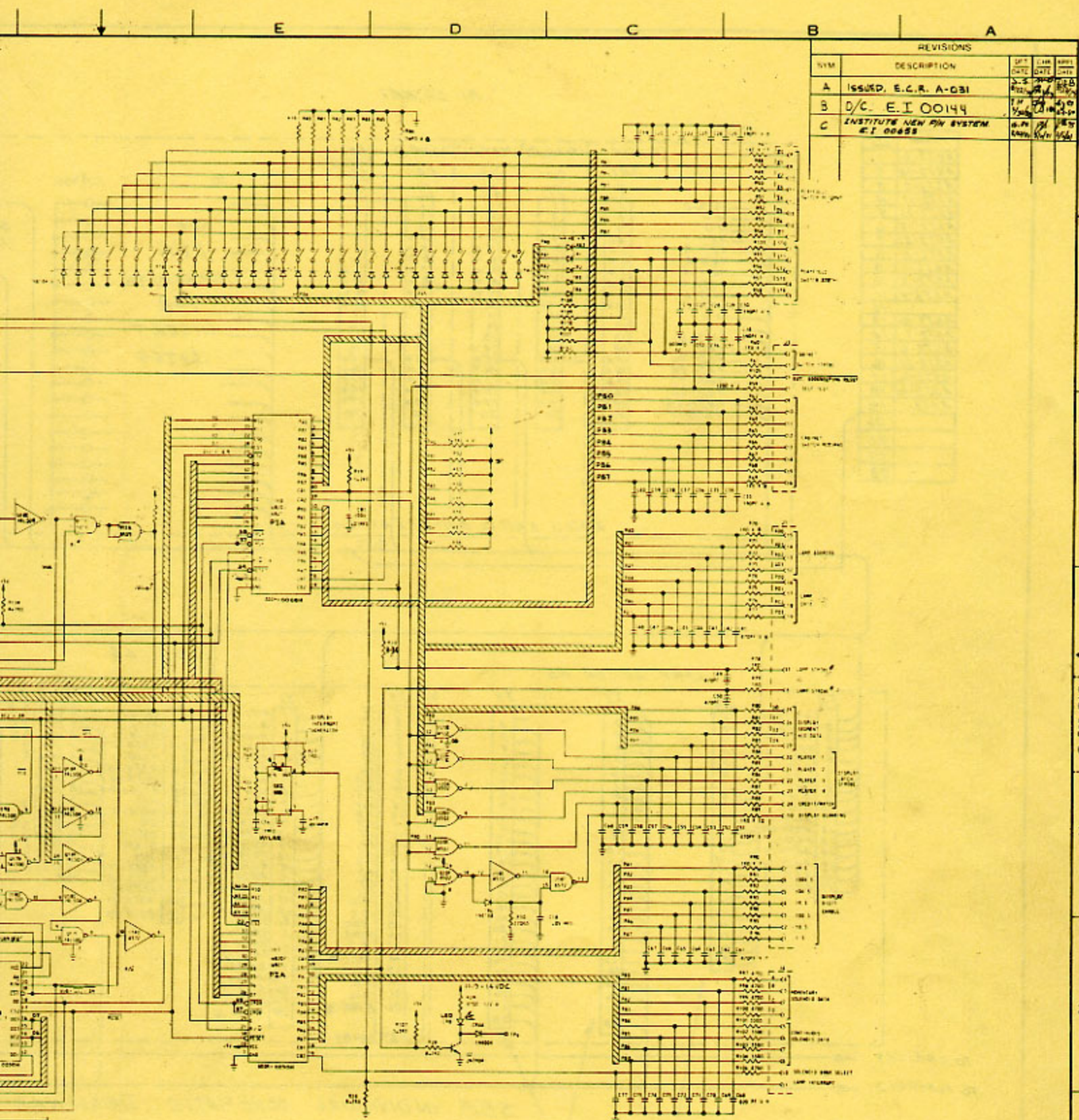
5

3

2

1





REVISIONS				
SYM	DESCRIPTION	DATE	BY	CHK
A	ISSUED, E.C.R. A-031	11/1/79	J.S.	J.S.
B	D/C. E.I. 00144	11/1/79	J.S.	J.S.
C	INSTITUTE NEW PIN SYSTEM E.I. 00655	11/1/79	J.S.	J.S.

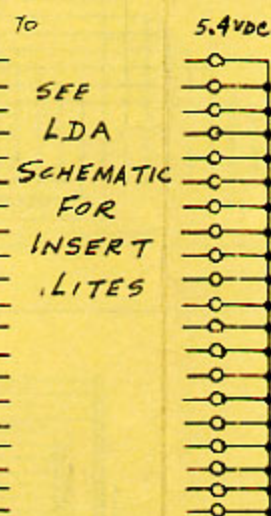
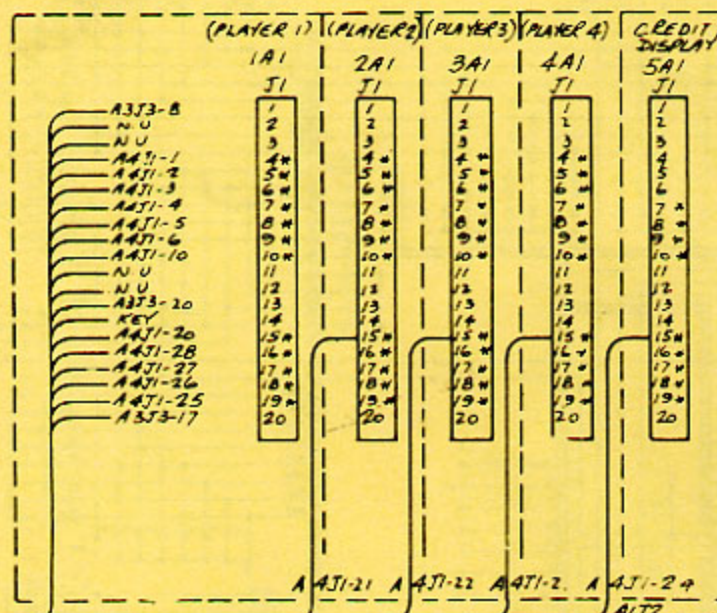
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MATERIAL
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ALL DIMENSIONS ARE AFTER PLATING

TOLERANCES UNLESS OTHERWISE SPECIFIED
FRACTIONS: 1/16, 1/32, 1/64, 1/128, 1/256, 1/512, 1/1024, 1/2048, 1/4096, 1/8192, 1/16384, 1/32768, 1/65536, 1/131072, 1/262144, 1/524288, 1/1048576, 1/2097152, 1/4194304, 1/8388608, 1/16777216, 1/33554432, 1/67108864, 1/134217728, 1/268435456, 1/536870912, 1/1073741824, 1/2147483648, 1/4294967296, 1/8589934592, 1/17179869184, 1/34359738368, 1/68719476736, 1/137438953472, 1/274877906944, 1/549755813888, 1/1099511627776, 1/2199023255552, 1/4398046511104, 1/8796093022208, 1/17592186044416, 1/35184372088832, 1/70368744177664, 1/140737488355328, 1/281474976710656, 1/562949953421312, 1/1125899906842624, 1/2251799813685248, 1/4503599627370496, 1/9007199254740992, 1/18014398509481984, 1/36028797018963968, 1/72057594037927936, 1/144115188075855872, 1/288230376151711744, 1/576460752303423488, 1/1152921504606846976, 1/2305843009213693952, 1/4611686018427387904, 1/9223372036854775808, 1/18446744073709551616, 1/36893488147419103232, 1/73786976294838206464, 1/147573952589676412928, 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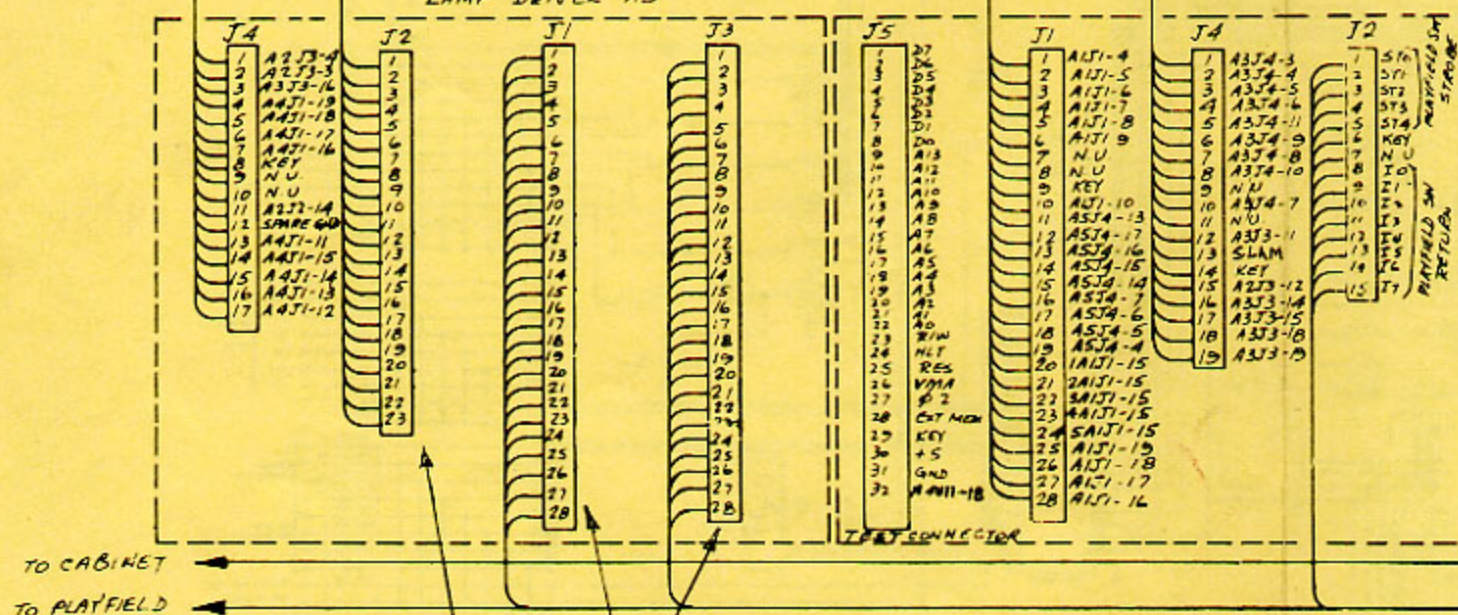
A1 INSERT

FROM	A1J2
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A5J2-14	2
A2J3-2	3
A2J3-11	4
A5J2-23	5
A5J2-15	6
A5J2-22	7
A5J2-8	8
A5J2-1	9
A5J2-2	10
A5J2-11	11
A5J2-10	12
A5J2-7	13
A5J2-14	14
A5J2-6	15
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A5J2-16	17
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A2J3-1	19
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	22
	23

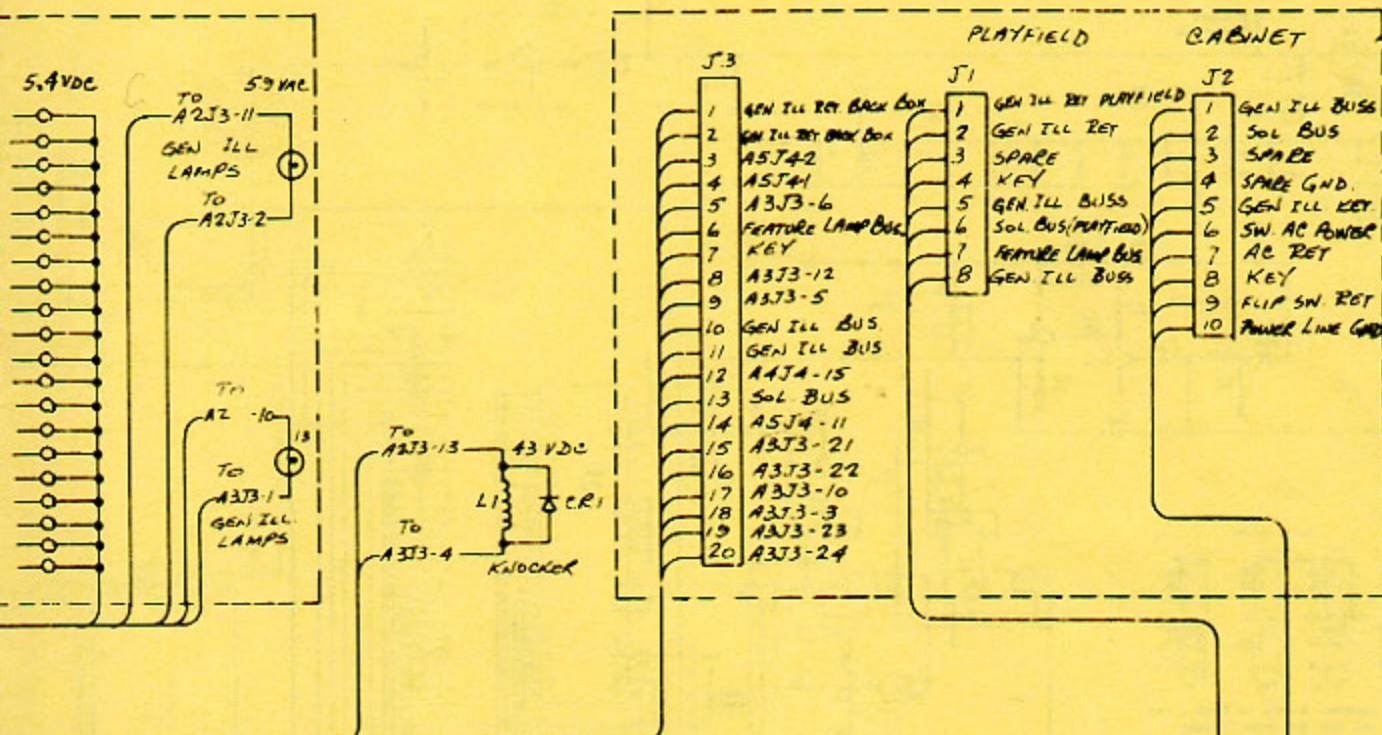


LAMP DRIVER A5

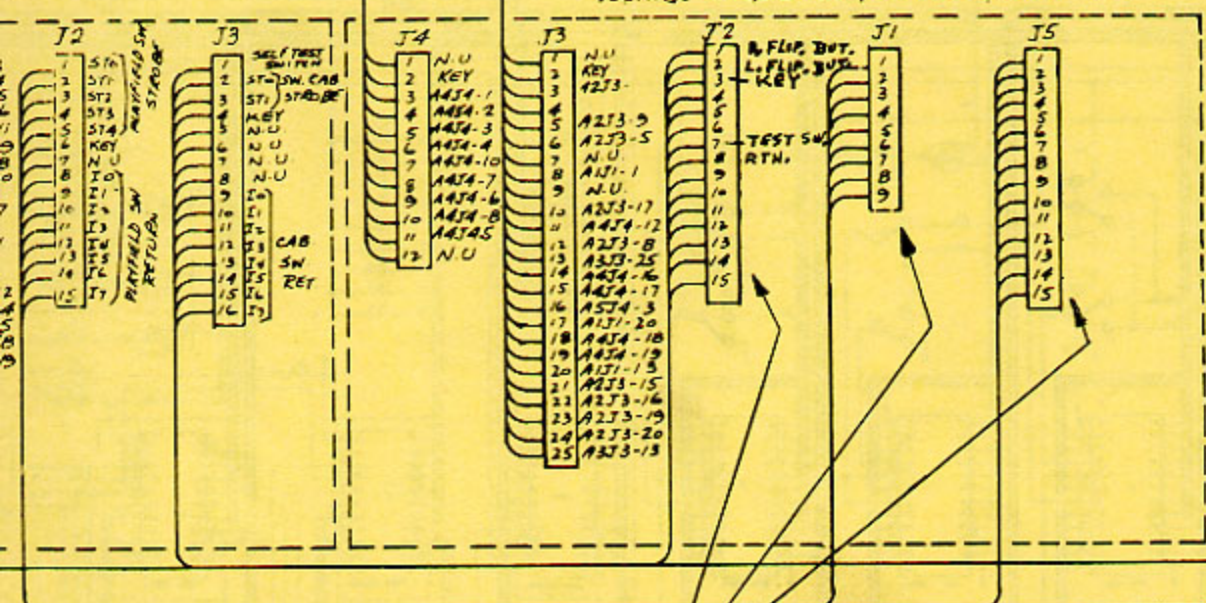
MPU - A4



A2 TRANSFORMER



VOLTAGE REGULATOR / SOLEN. DRIVER A3



DRAWINGS FOR WIRE COLORS.

SEE SDU SCHEMATIC
FOR SOLENOID PIN OUTS.

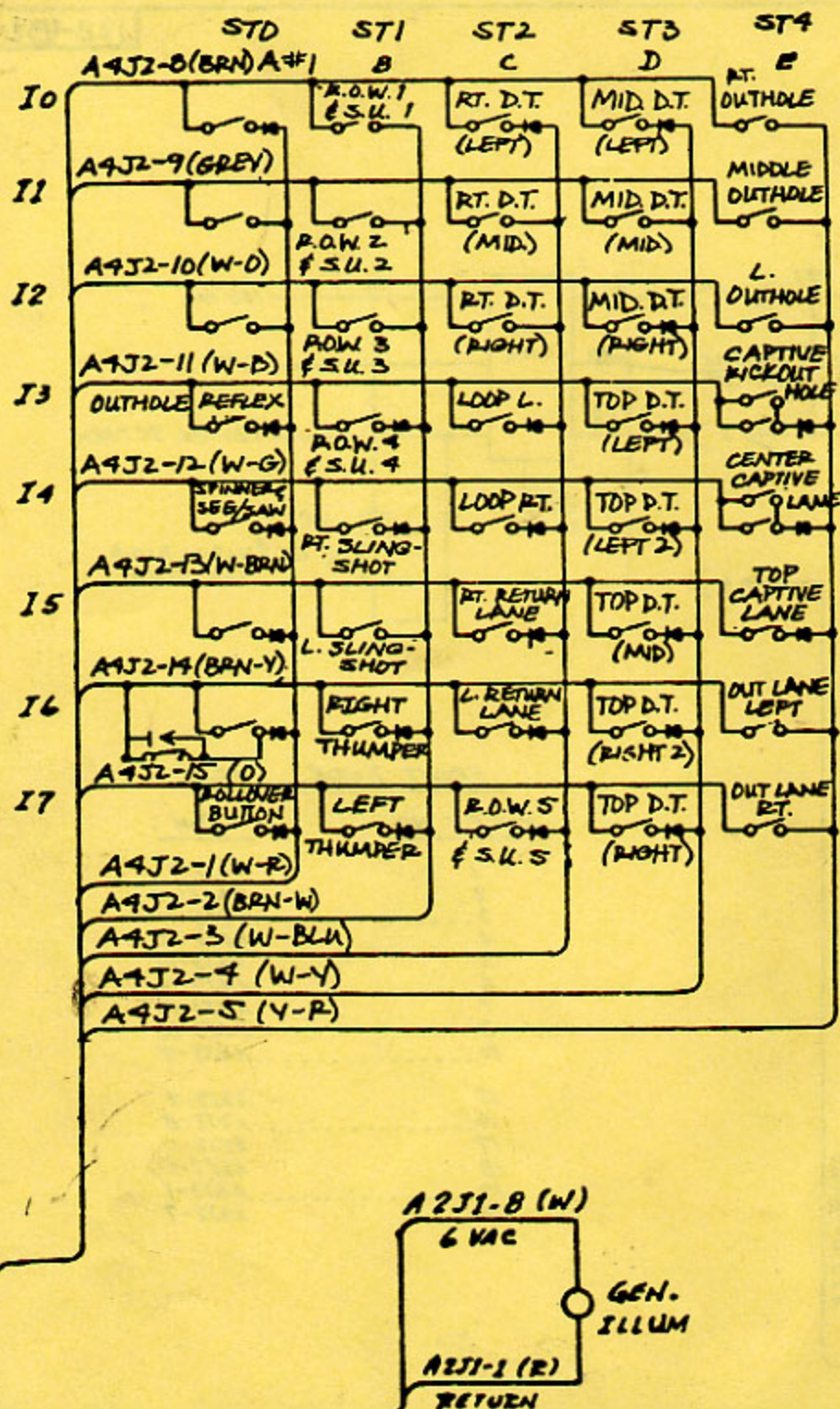
STERN ELECTRONICS INC.

1725 DIVERSEY BLVD. CHICAGO 60614

WIRING DIAGRAM

FOR SOLID STATE

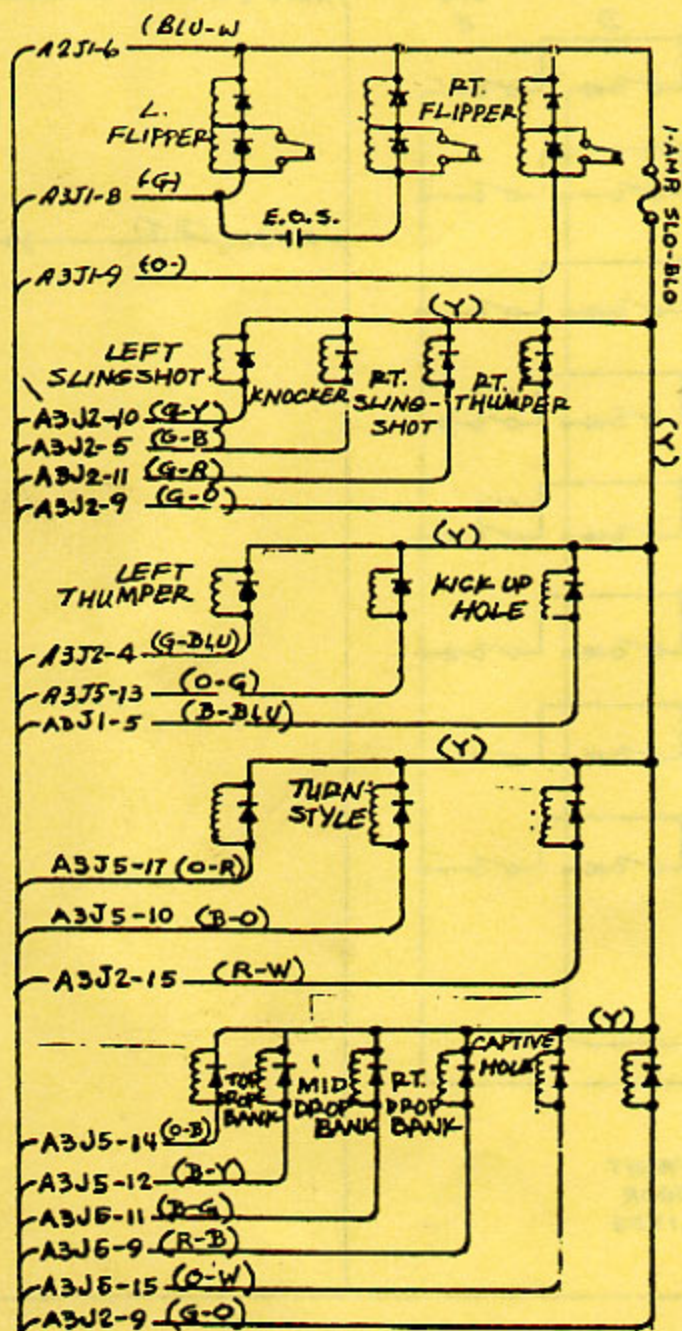
DRN. CW.	DATE	AMT. CW.
MAT. CAP.	FIN. CAP.	APPR.

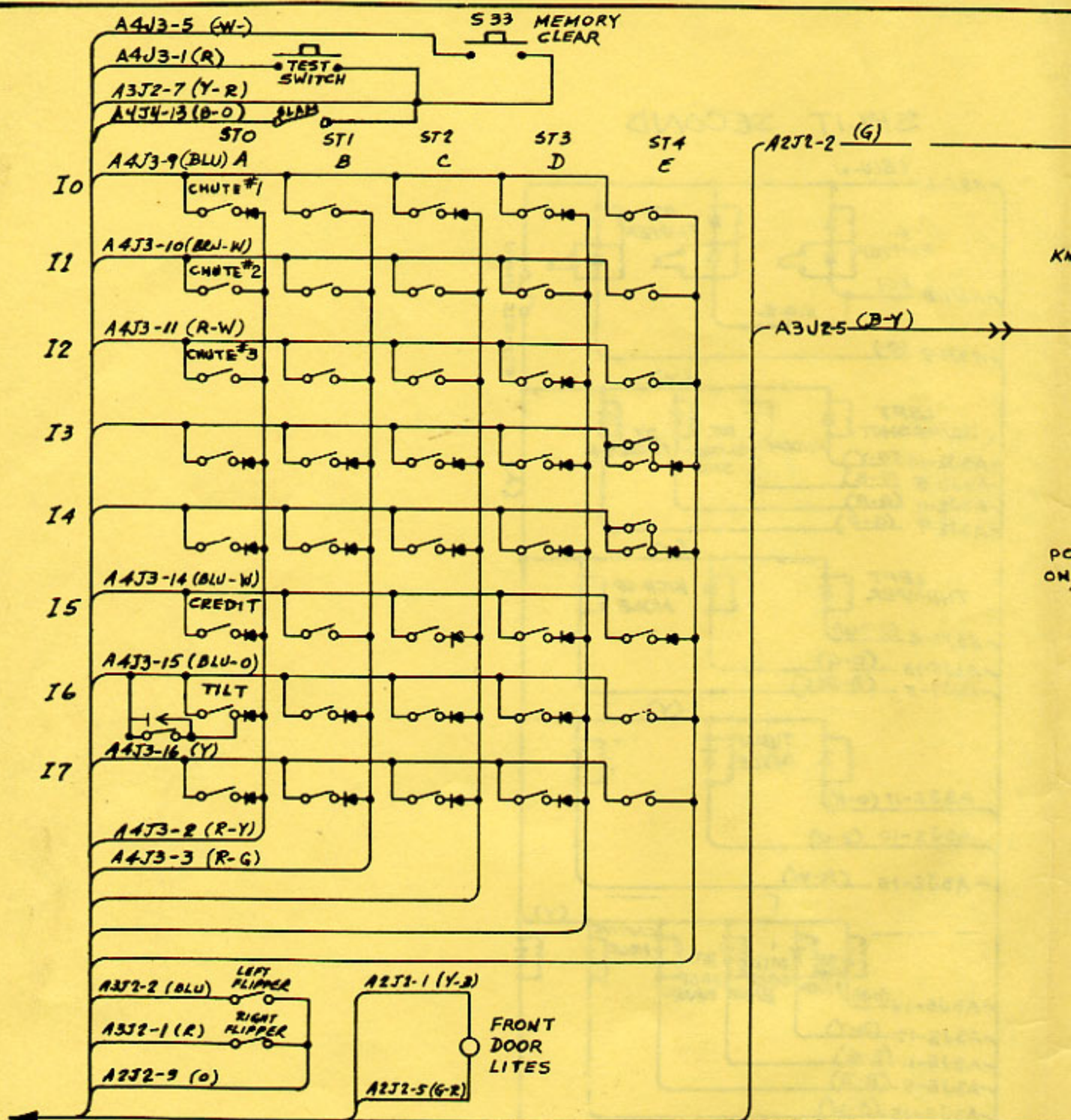


D.T. = DROP TARGET
P.O.W. = ROLL OVER WIRE
S.U. = STAND-UP TARGET

PLAYFIELD
SPLIT SECOND

SPLIT SECOND



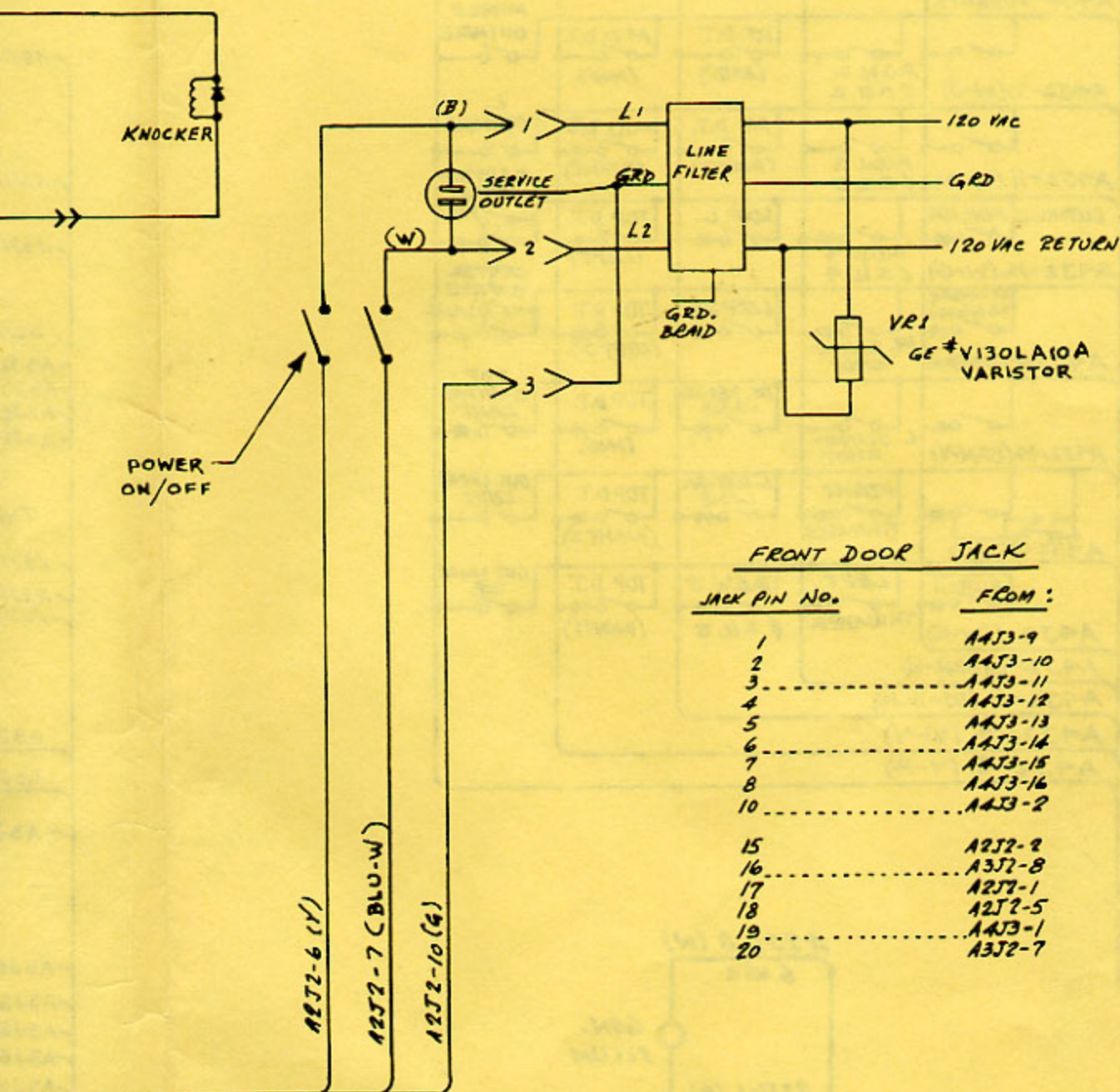


N/U = NOT USED
 ALL DIODES ARE IN-4148

12B-432-121-1

DIETZEN NO. 1000 ASBPROOF

ISSUE	CHANGE



CHANGE	DATE

STERN ELECTRONICS INC.		
1725 DIVERSEY BLVD. CHICAGO 80614		
CABINET AND FRONT DOOR WIRING		
FOR STANDARD		
DRN. C.W.	DATE 10-16-79	AMT. 45
MAT. 45	FIN. 45	APPR.